

ARMANI

MV Build

2010.01.20

DATE	CHANGE NO.	REV

EE		DATE		POWER		DATE		INVENTEC A POWER OF INNOVATION							
DRAWER												TITLE ARMANI			
DESIGN															
CHECK															
RESPONSIBLE															
SIZE = 3								VER:		SIZE		CODE	DOC. NUMBER	REV	
FILE NAME : XXXX-XXXXXX-XX										A3		CS		X01	
P/N XXXXXXXXXX										SHEET		1	OF	61	

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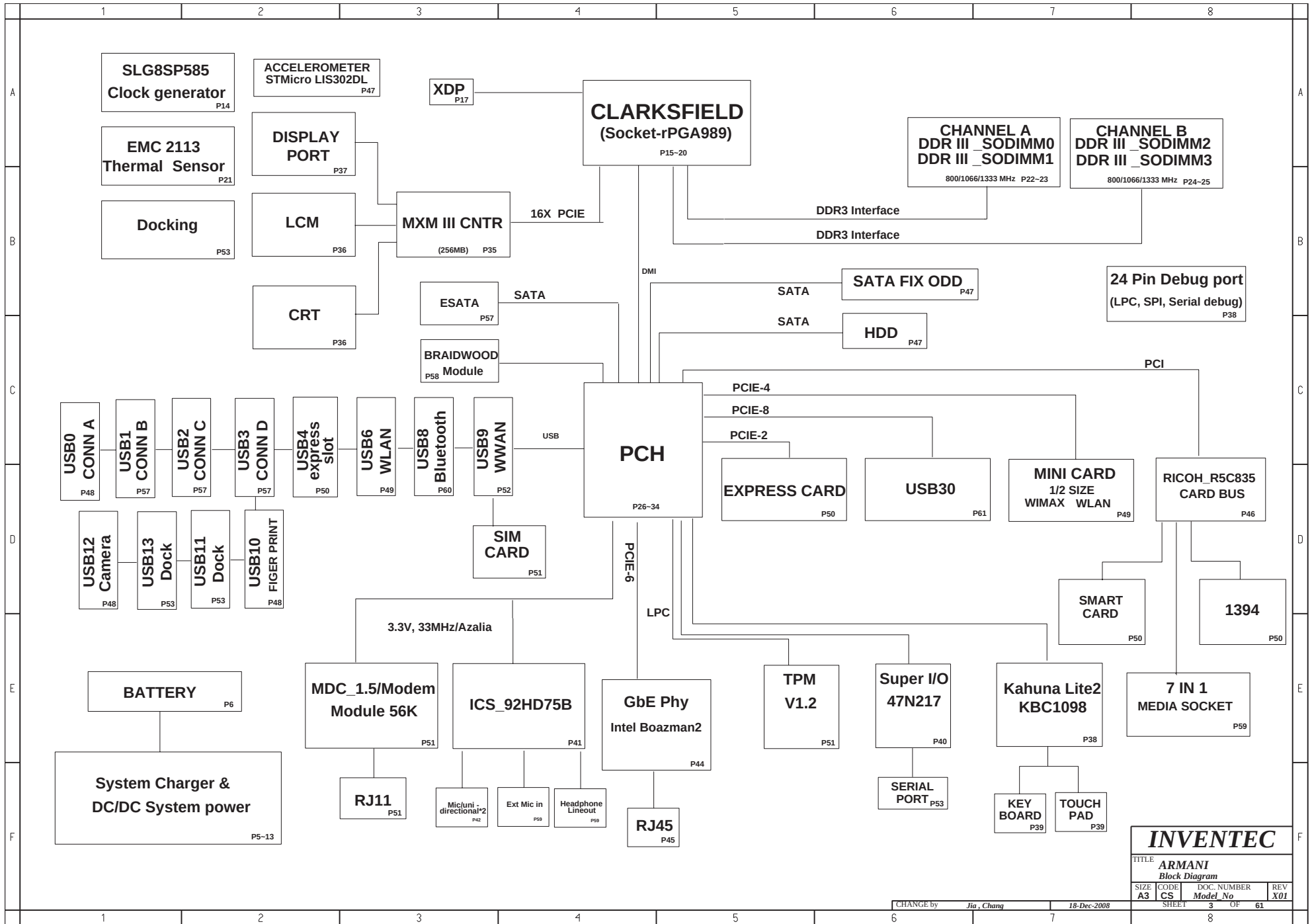
INVENTEC

TITLE
ARMANI

SIZE A3	CODE CS	DOC. NUMBER Model No	REV X01
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CHANGE by Jia, Chang 18-Dec-2008

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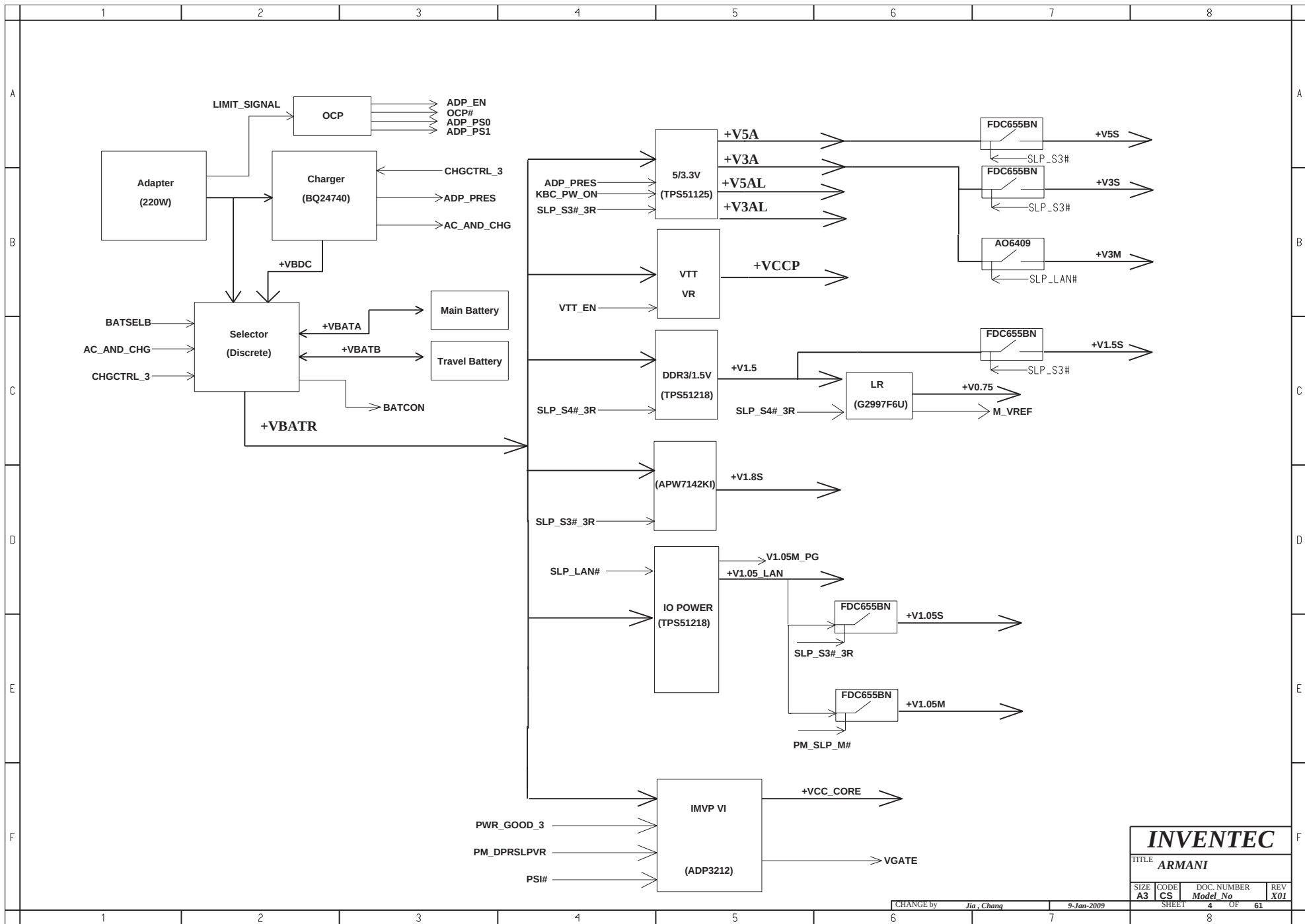


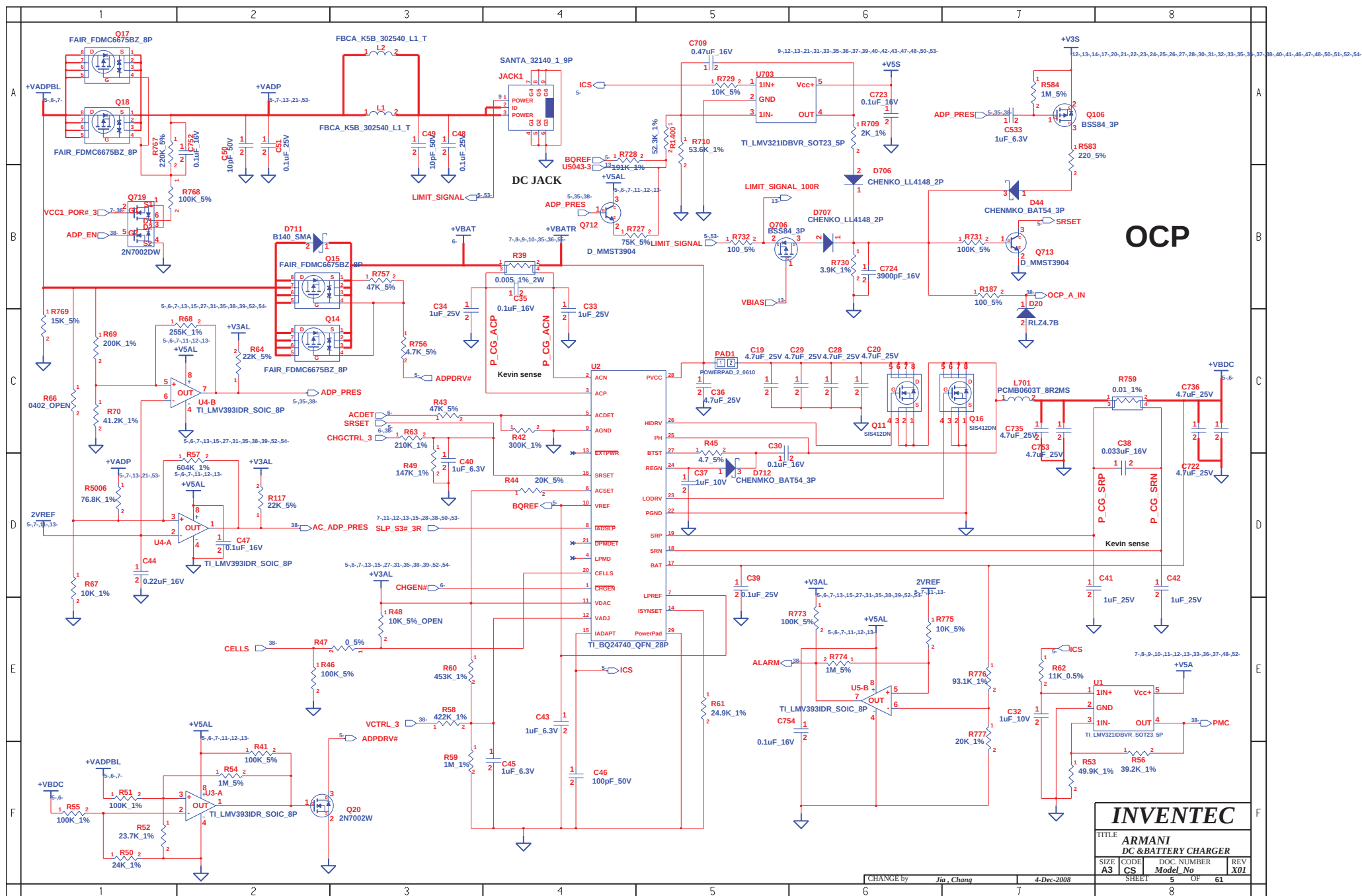
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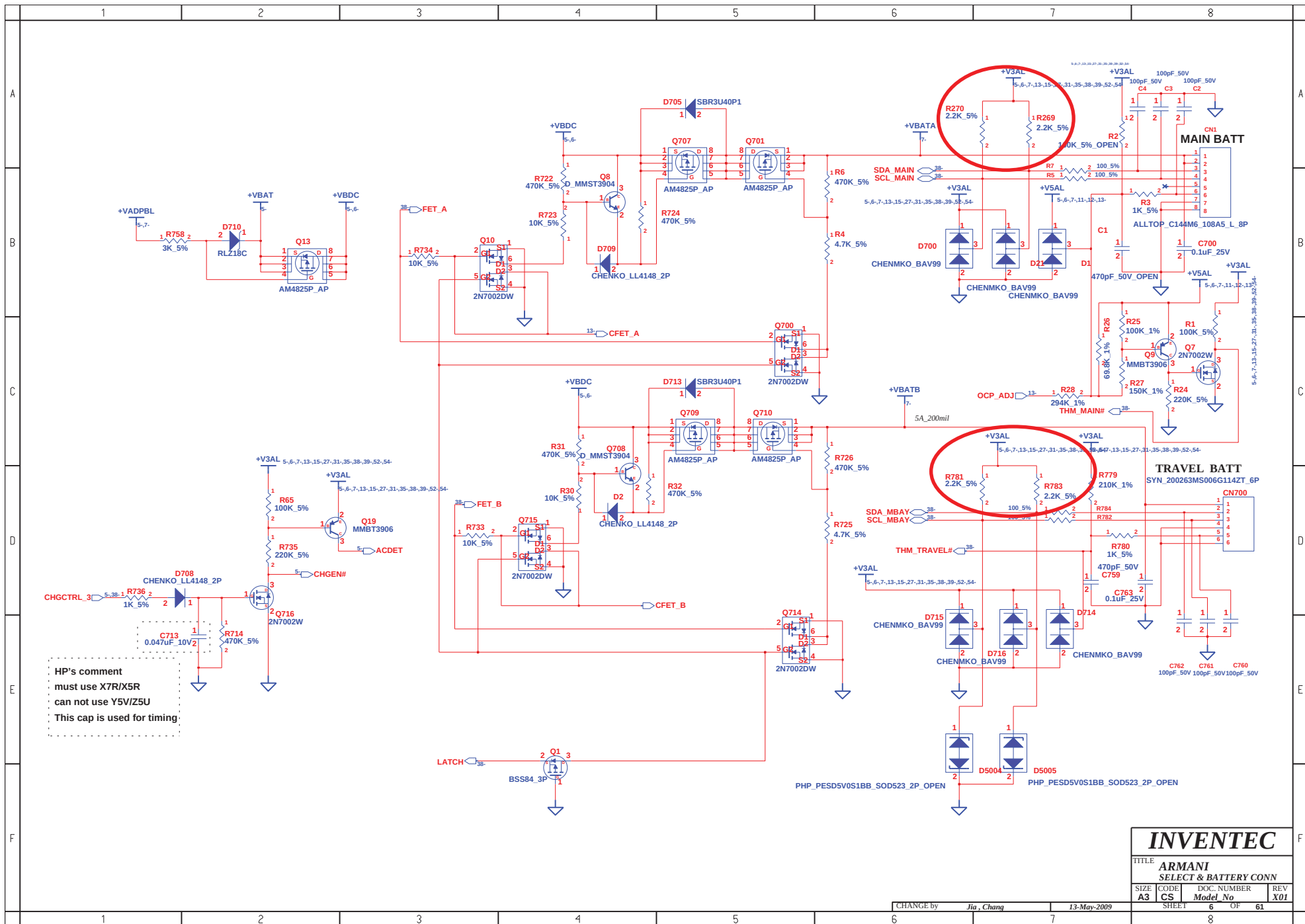
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ARMANI			
Block Diagram			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

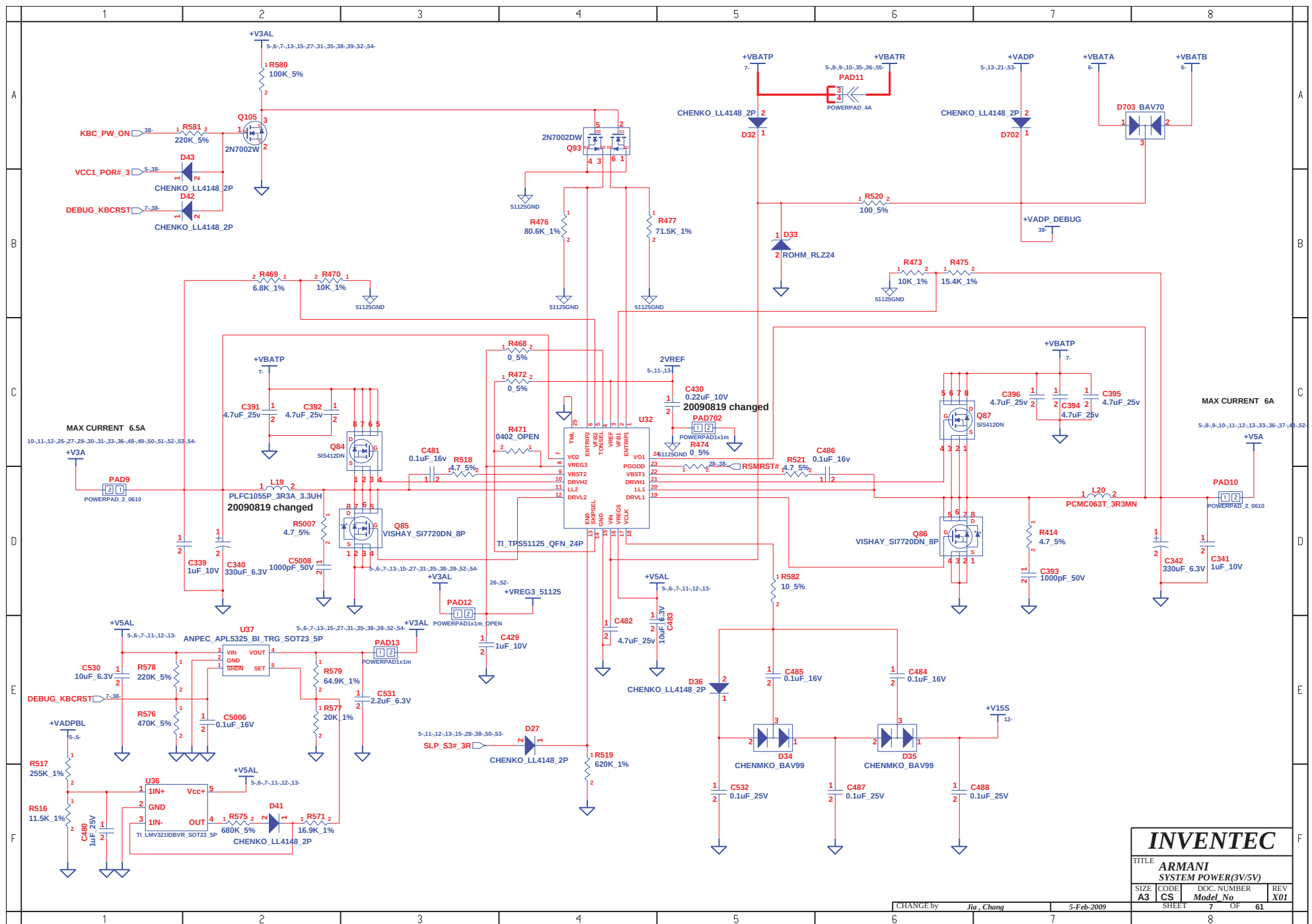
CHANGE by Jia, Chang 18-Dec-2008

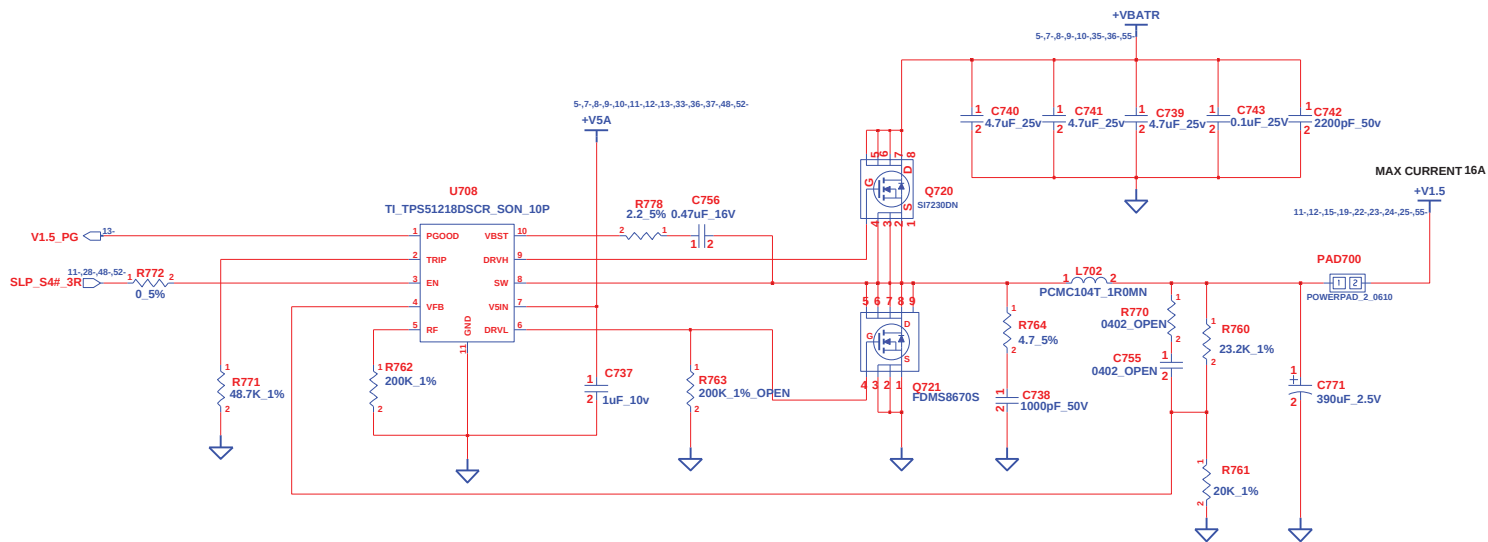
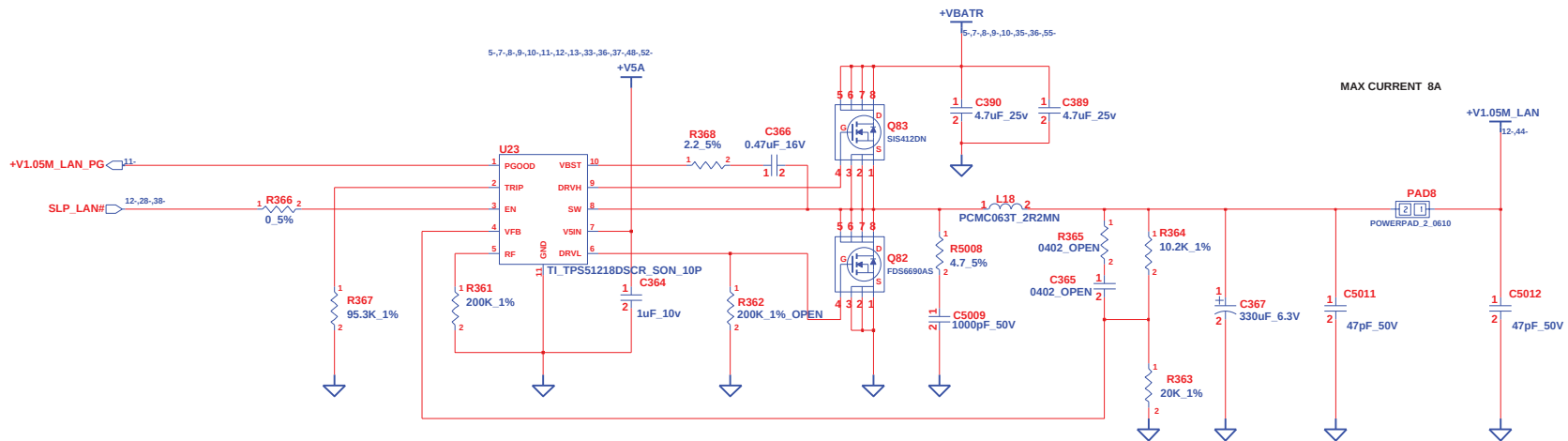
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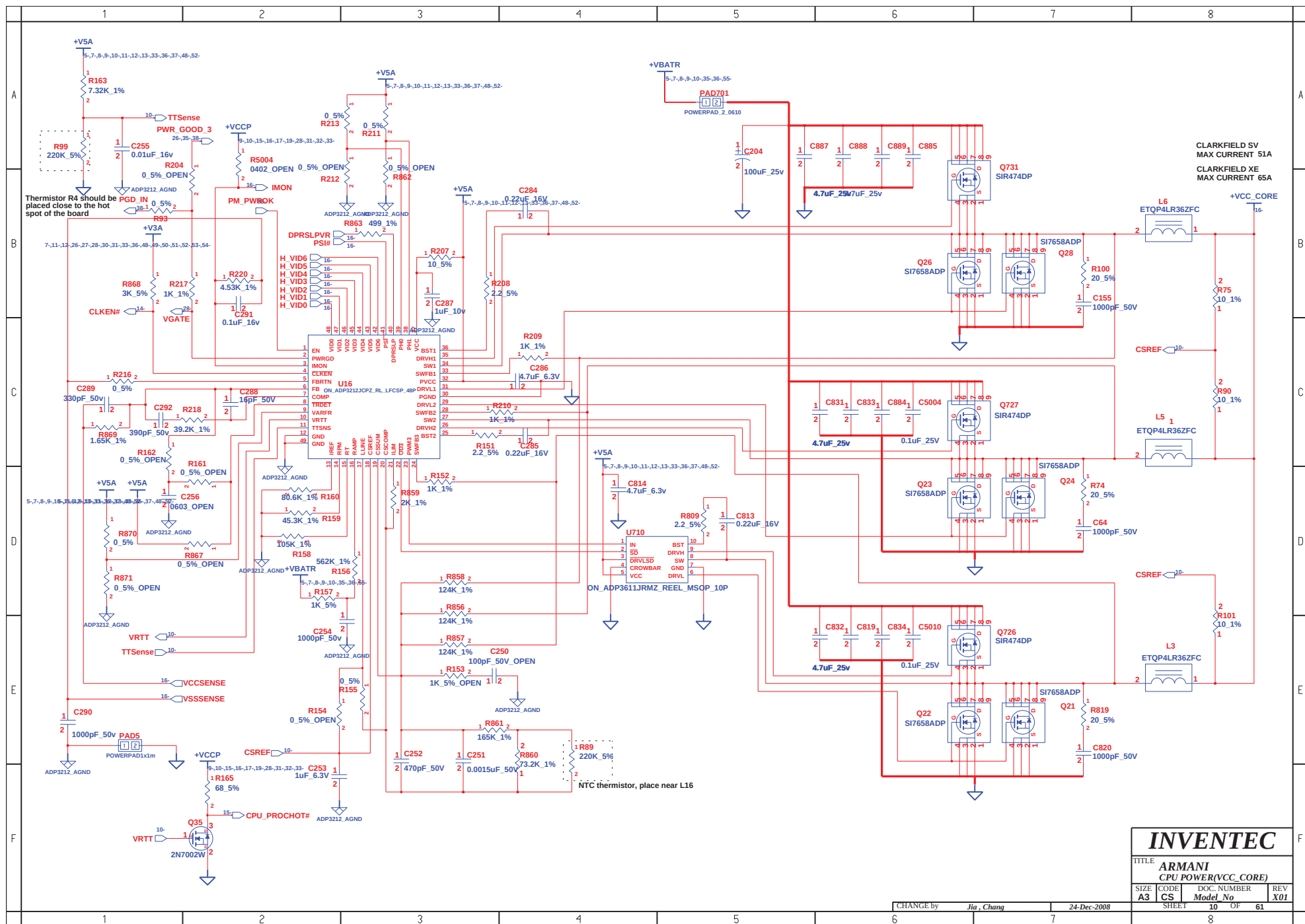


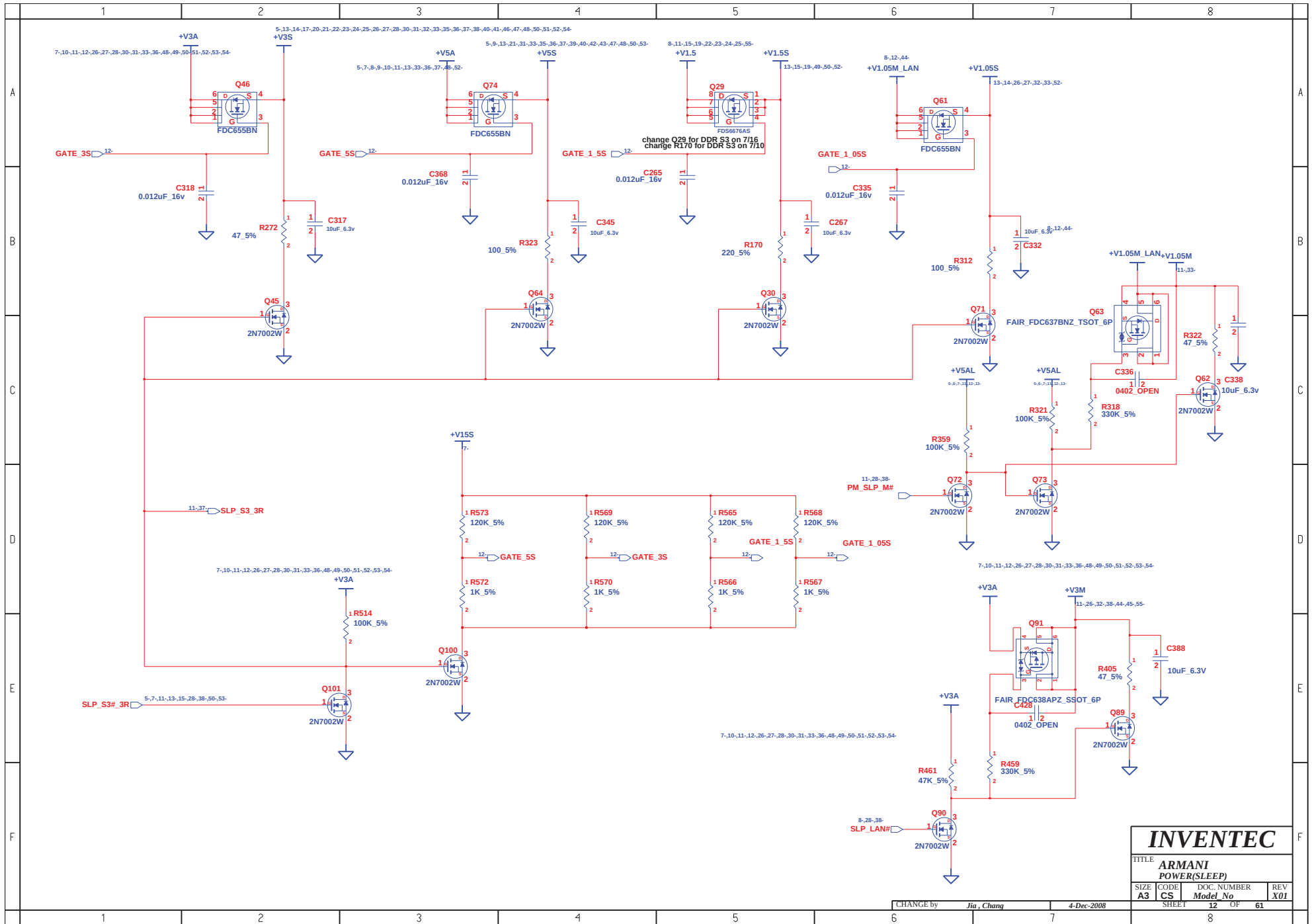




INVENTEC			
TITLE ARMANI			
SYSTEM POWER(+V1.05M/+VCCP)			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV X01
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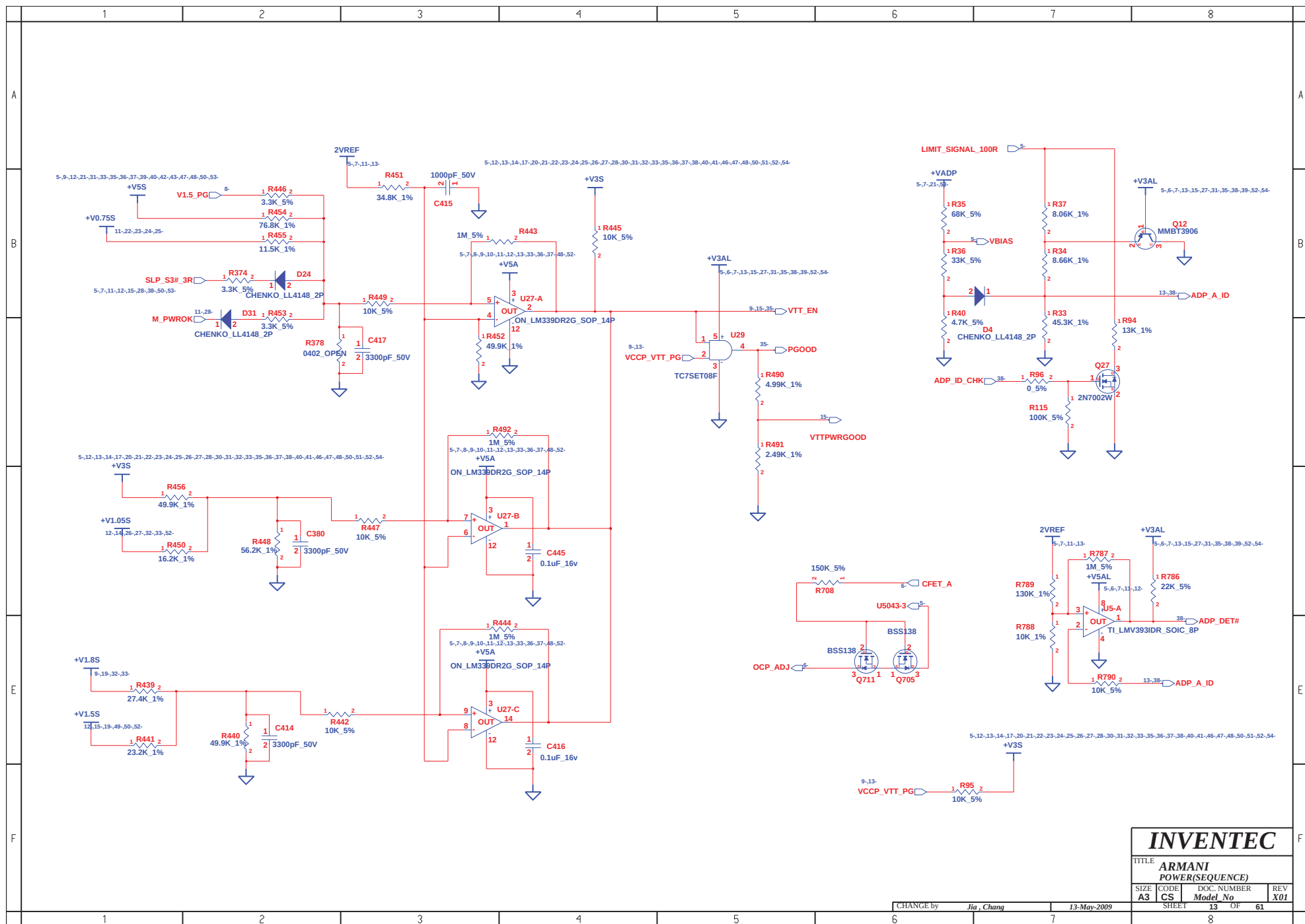
CHANGE by Jia, Chang 24-Dec-2008





INVENTEC

TITLE ARMANI POWER(SLEEP)			
SIZE A3	CODE CS	DOC. NUMBER Model_No	REV X01
CHANGE by Jia, Chang		4-Dec-2008	
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12,13,14,26,27,32,33,52-

+V1.05S

L16
BLM18AG471SN1D

C362
10uF_6.3v

Layout note: All decoupling 0.1uF disperse closed to pin

C926 1 2 0.1uF_16v
C925 1 2 0.1uF_16v
C361 1 2 0.1uF_16v

C360
2 47pF_50v

5,12,13,14,17,20,21,22,23,24,25,26,27,28,30,31,32,33,35,36,37,38,40,41,46,47,48,50,51,52,54-

+V3S

R331
10K_5%

CLK_DREF# 27-
CLK_DREF# 27-
CLK_DREF# 27-
CLK_SATA1# 27-
CLK_SATA1# 27-
CLK_PCIE_PCH# 27-
CLK_PCIE_PCH# 27-

+VCCP_32

+V3S

5,12,13,14,17,20,21,22,23,24,25,26,27,28,30,31,32,33,35,36,37,38,40,41,46,47,48,50,51,52,54-

Layout note: All decoupling 0.1uF disperse closed to pin

L17
BLM18AG471SN1D

C358
10uF_6.3v

+V3S_CLOCKIO

C928 1 2 0.1uF_16v
C357 1 2 0.1uF_16v
C927 1 2 0.1uF_16v
C359 1 2 0.1uF_16v
C356 1 2 0.1uF_16v
C379 1 2 47pF_50v
C929

U22
VDD_DOT
VSS_DOT
DOT_96
DOT96_#
VDD_27
27MHz
VSS_27
27MHz_SS
VSS_SATA
SRC_1-SATA
SRC_1-SATA#
VSS_SRC
SRC_2
SRC_2#
VDD_SRC_IO
CPU_STOP#
TML-PAD

SLG_SLG8SP585_QFN_32P

SCL 32
SDA 31
REF_0-CPU_SEL 29
VDD_REF 28
XTAL_IN 27
XTAL_OUT 26
VSS_REF 25
CKPWGRGD_PCH# 24
VDD_CPU 23
CPU_0# 22
CPU_0# 21
VSS_CPU 20
CPU_1# 19
CPU_1# 18
VDD_CPU_IO 17
VDD_SRC 16

BCLK_PCH 27-
BCLK_PCH# 27-

R375
10K_5%_OPEN

R377
10K_5%

R376
10K_5%

+V1.05S

C1435
18pF_50v

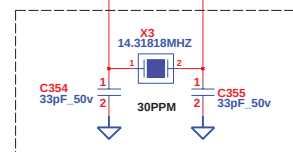
12,13,14,26,27,32,33,52-

5,12,13,14,17,20,21,22,23,24,25,26,27,28,30,31,32,33,35,36,37,38,40,41,46,47,48,50,51,52,54-

R332
10K_5%

Q56
2N7002

CLKEN#



CPU Frequency Select Table

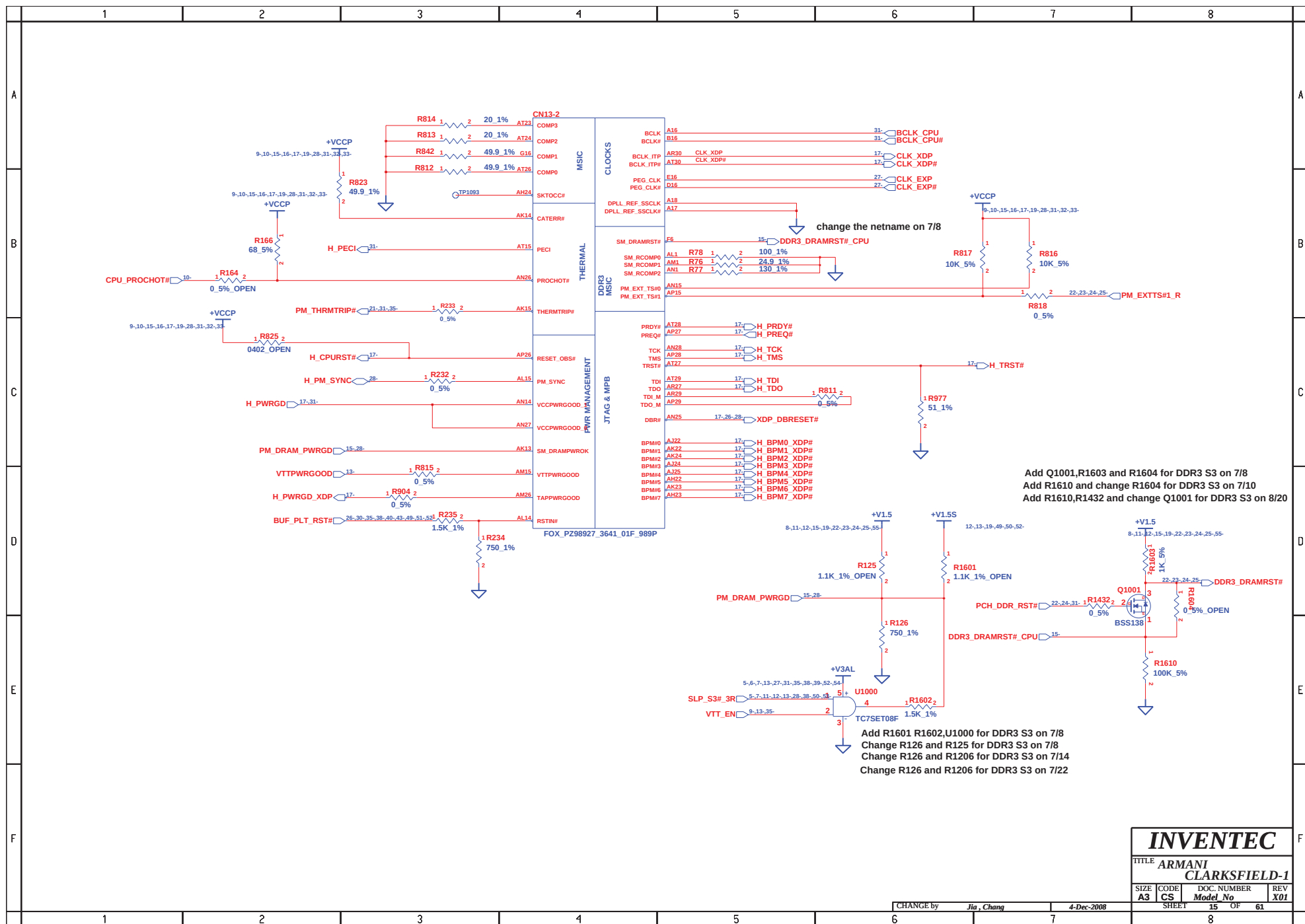
FSLC Bob7	CPU MHz	SRC MHz	REF MHz	USB MHz	DOT MHz
0(Default)	133.33	100.00	14.318	48.00	96.00
1	100.00				

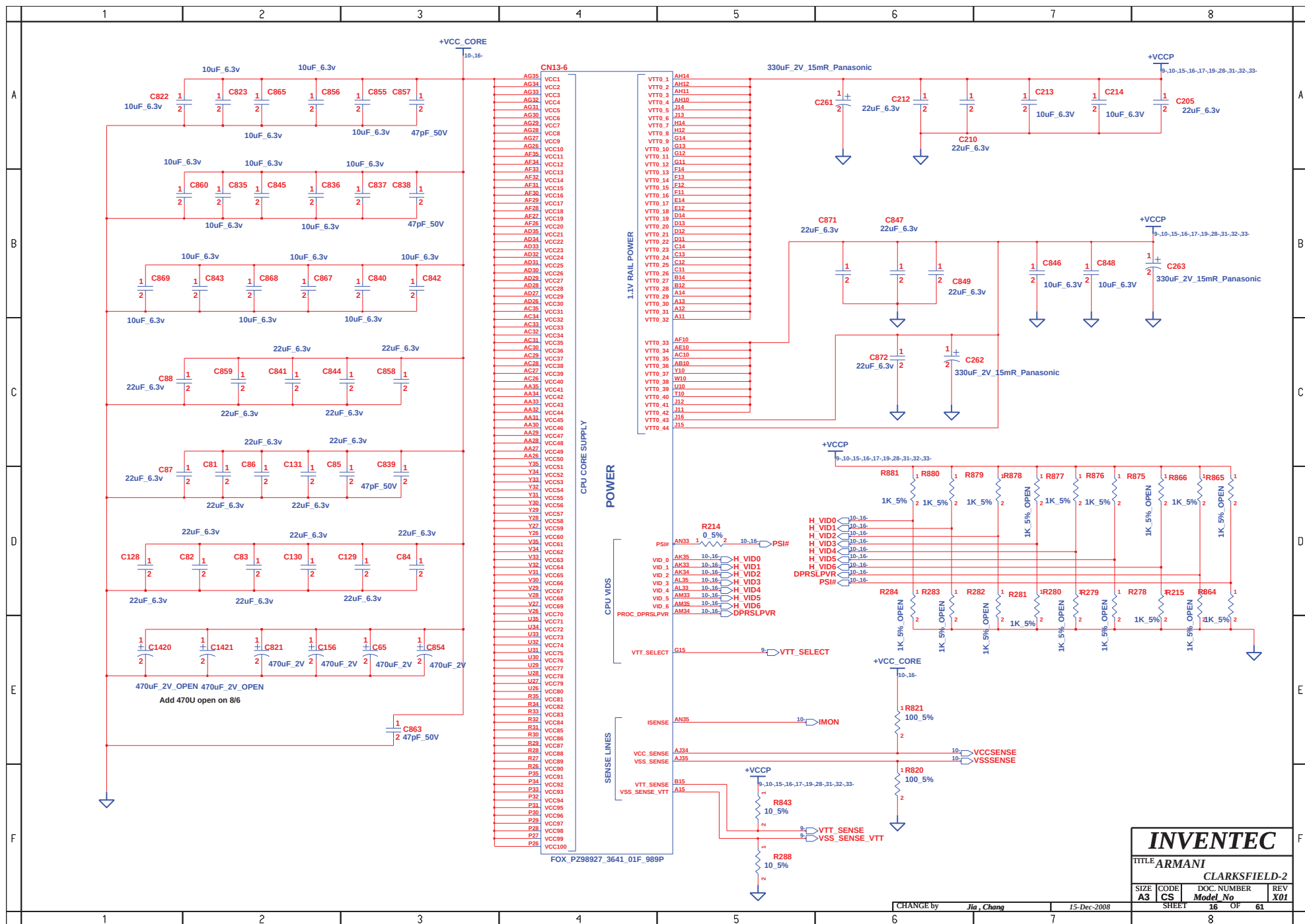
INVENTEC

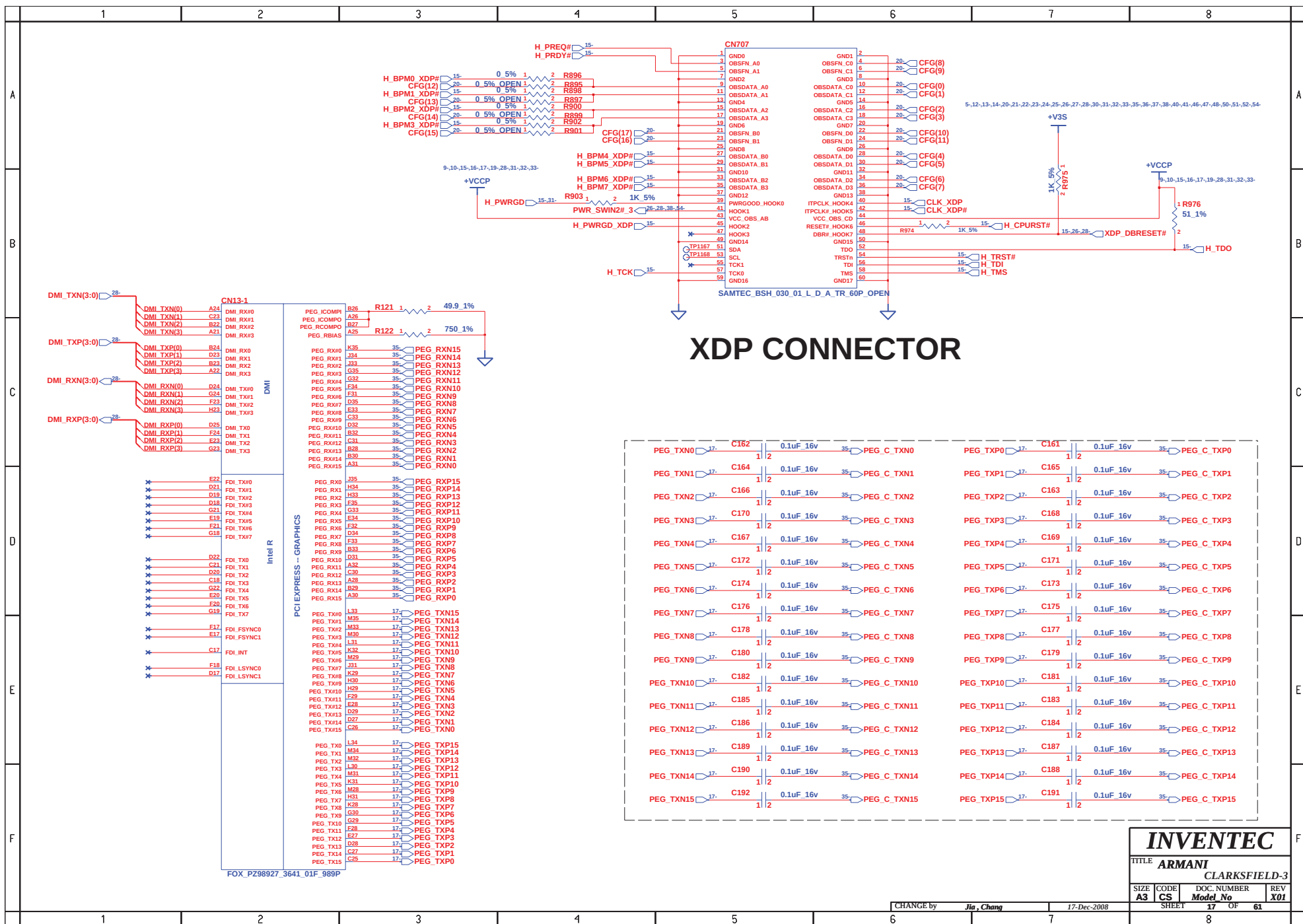
TITLE	ARMANI CLOCK_GENERATOR		
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

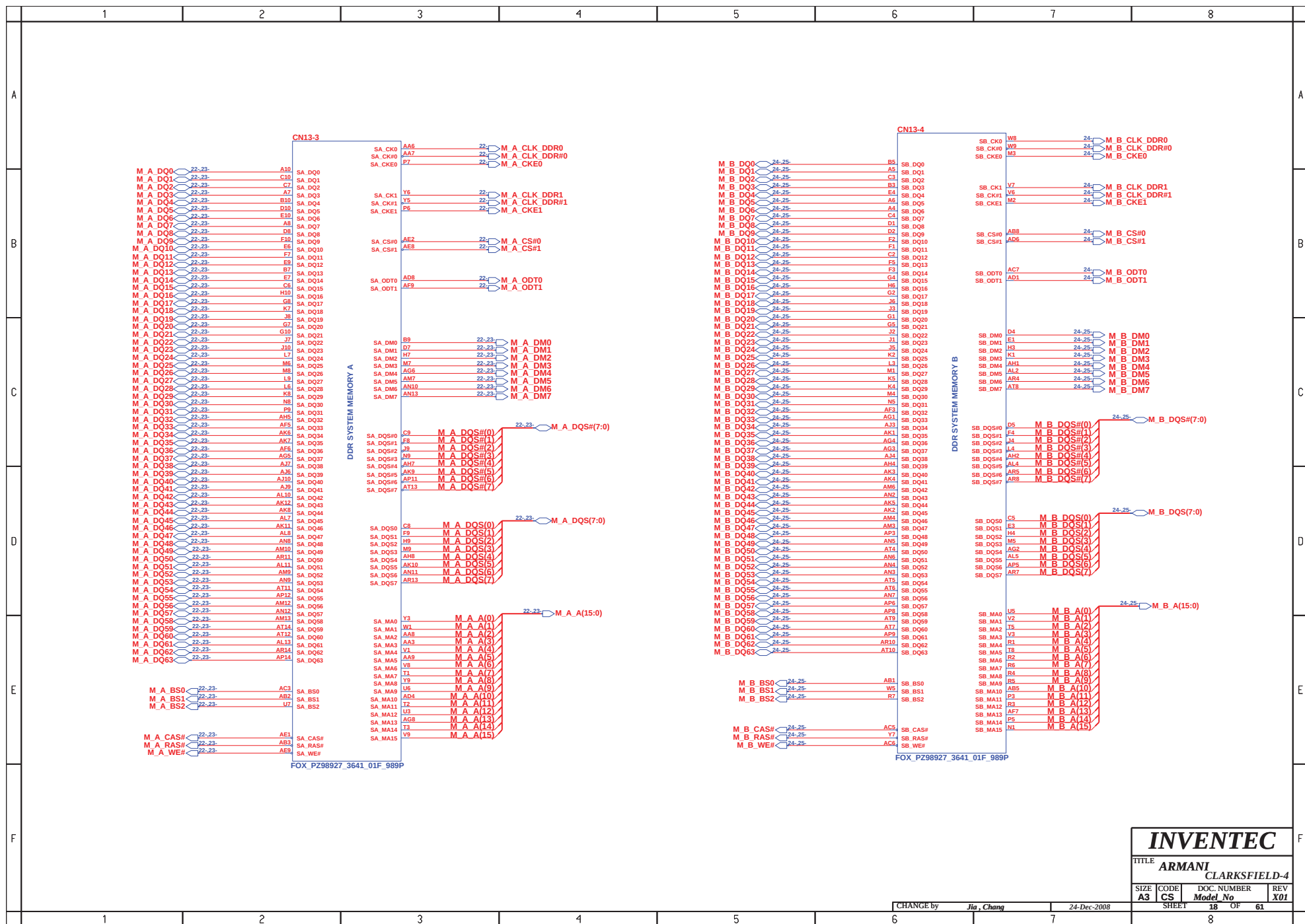
CHANGE by Jia, Chang 19-Dec-2008

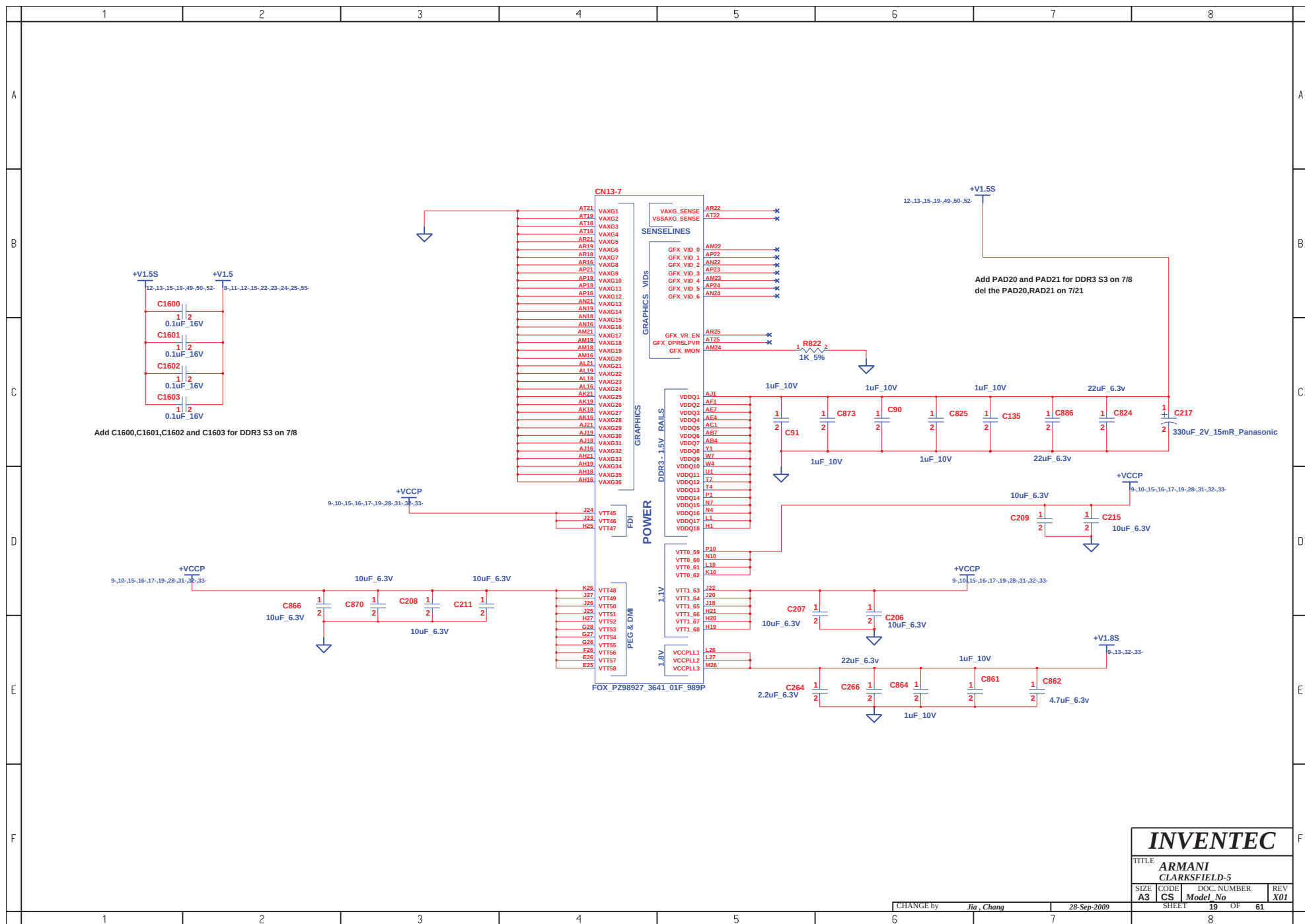
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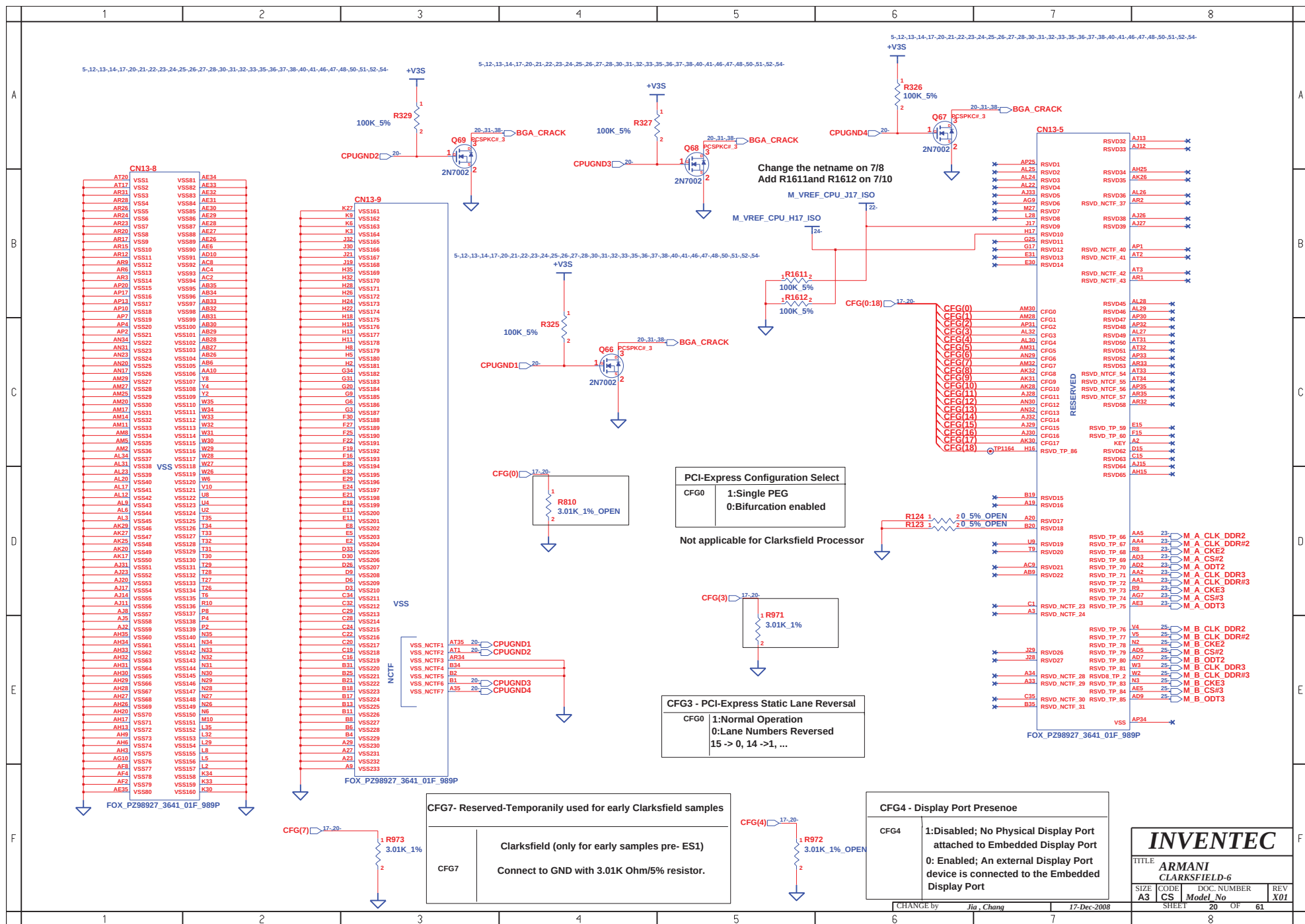


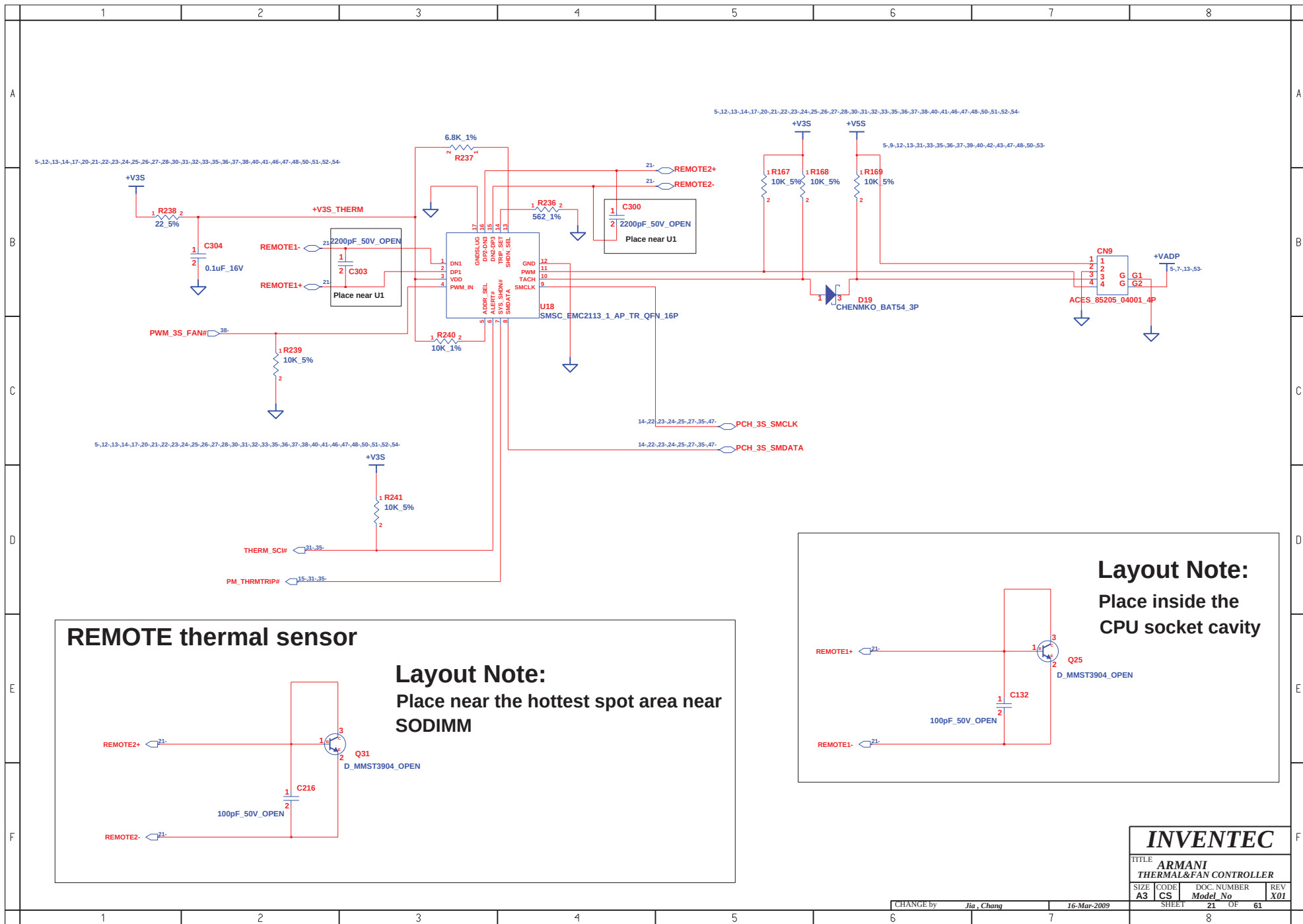


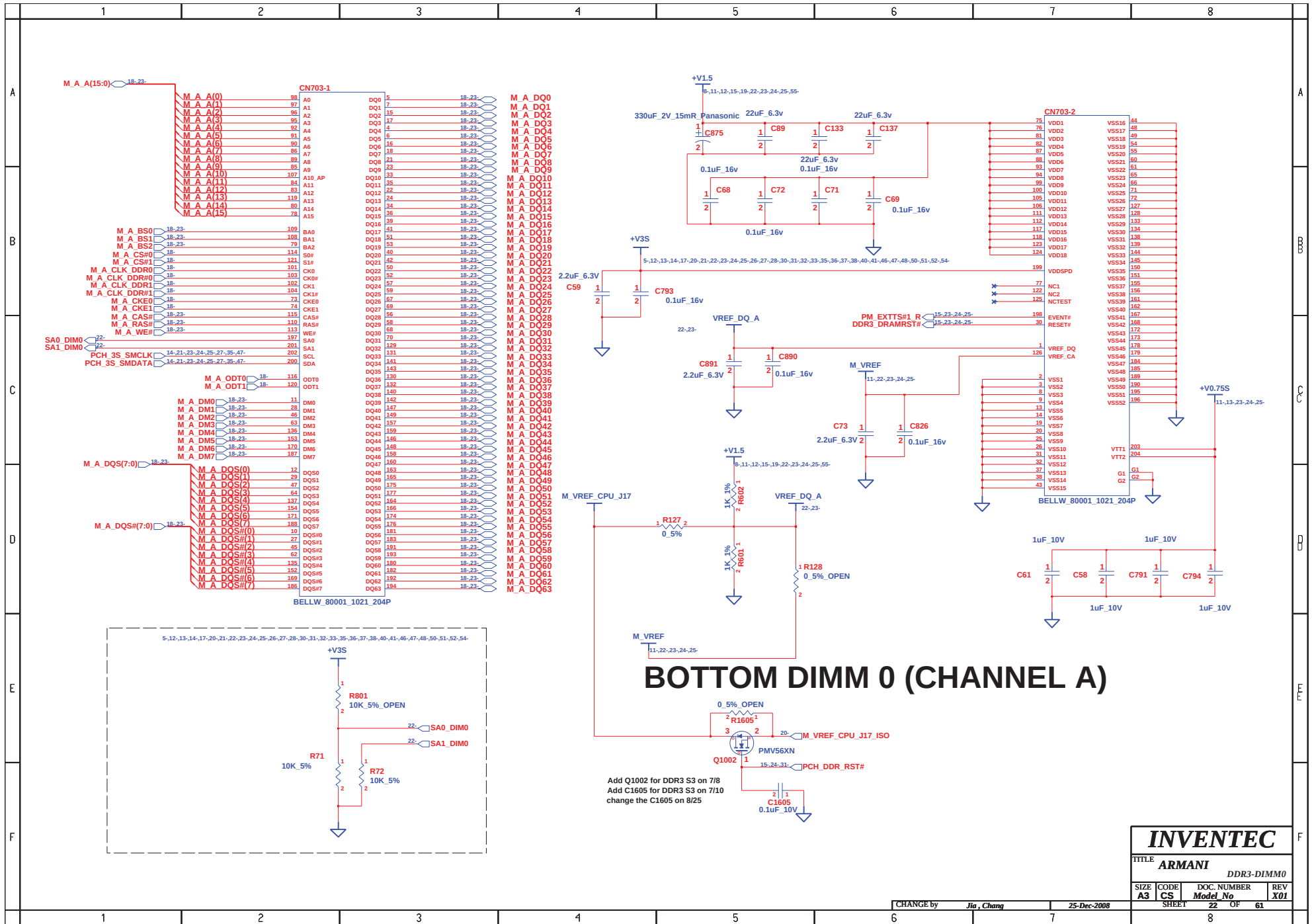












BOTTOM DIMM 0 (CHANNEL A)

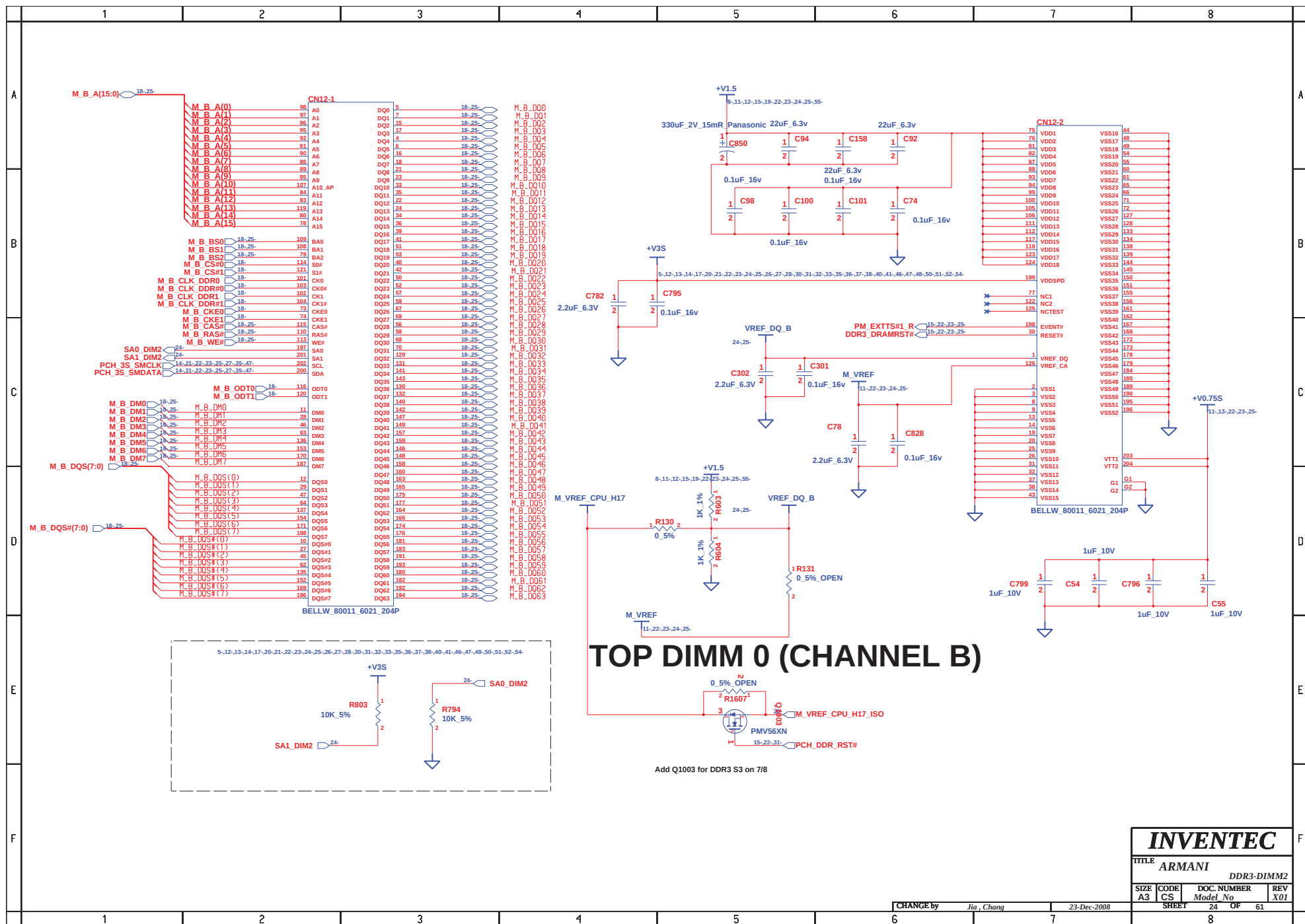
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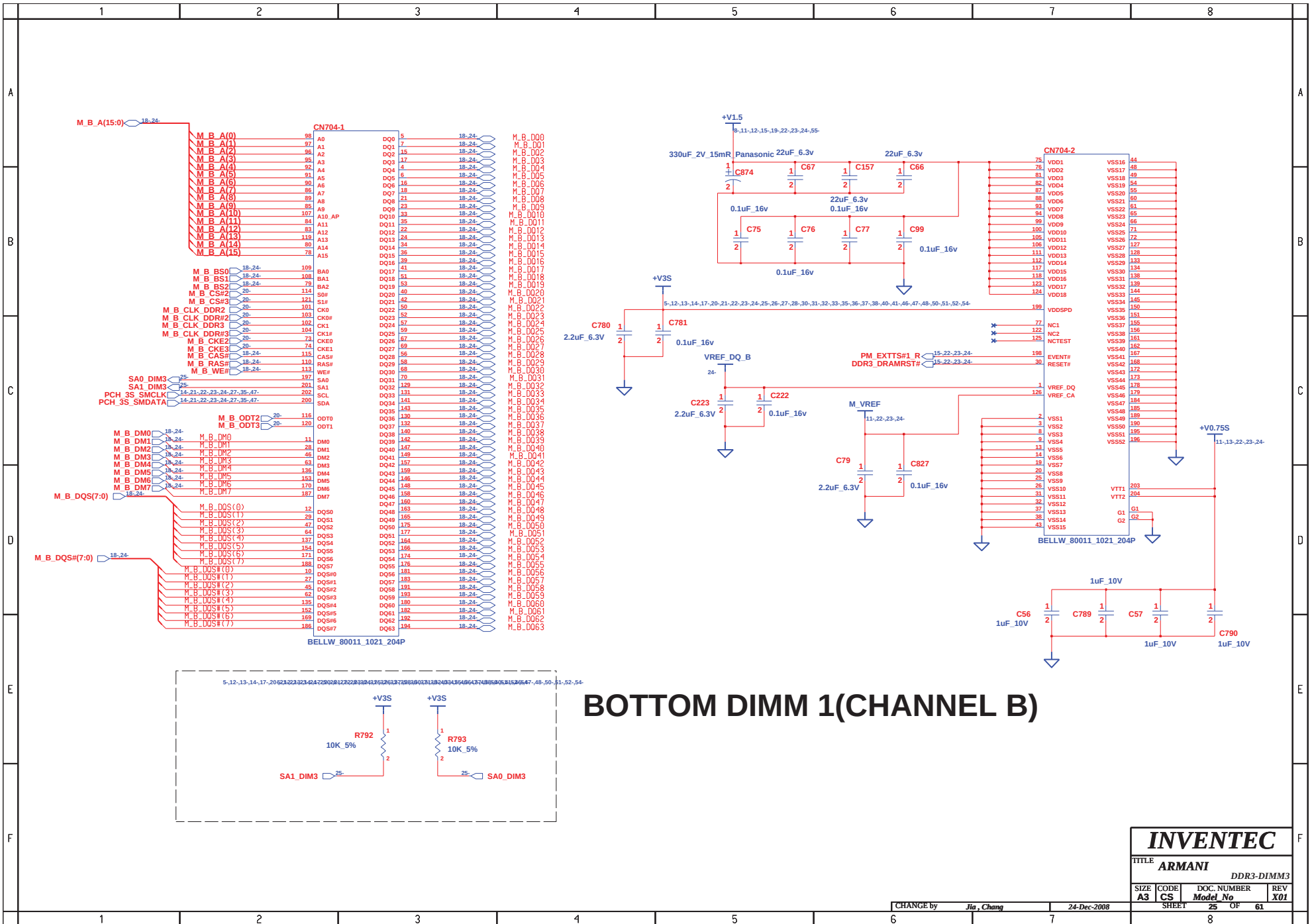
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SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

CHANGE by **Jin, Chang** **25-Dec-2008**

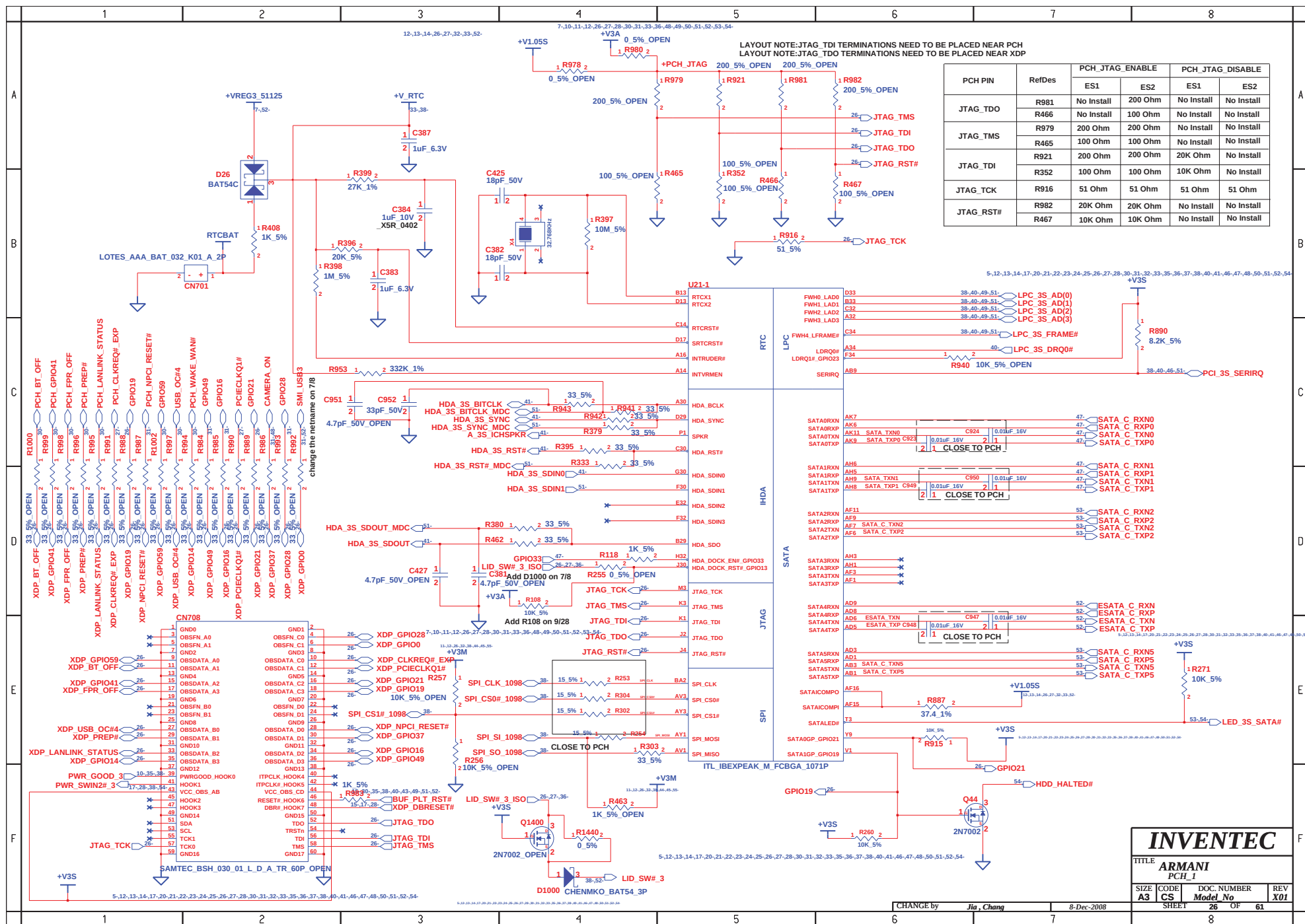
SHEET **22** OF **61**

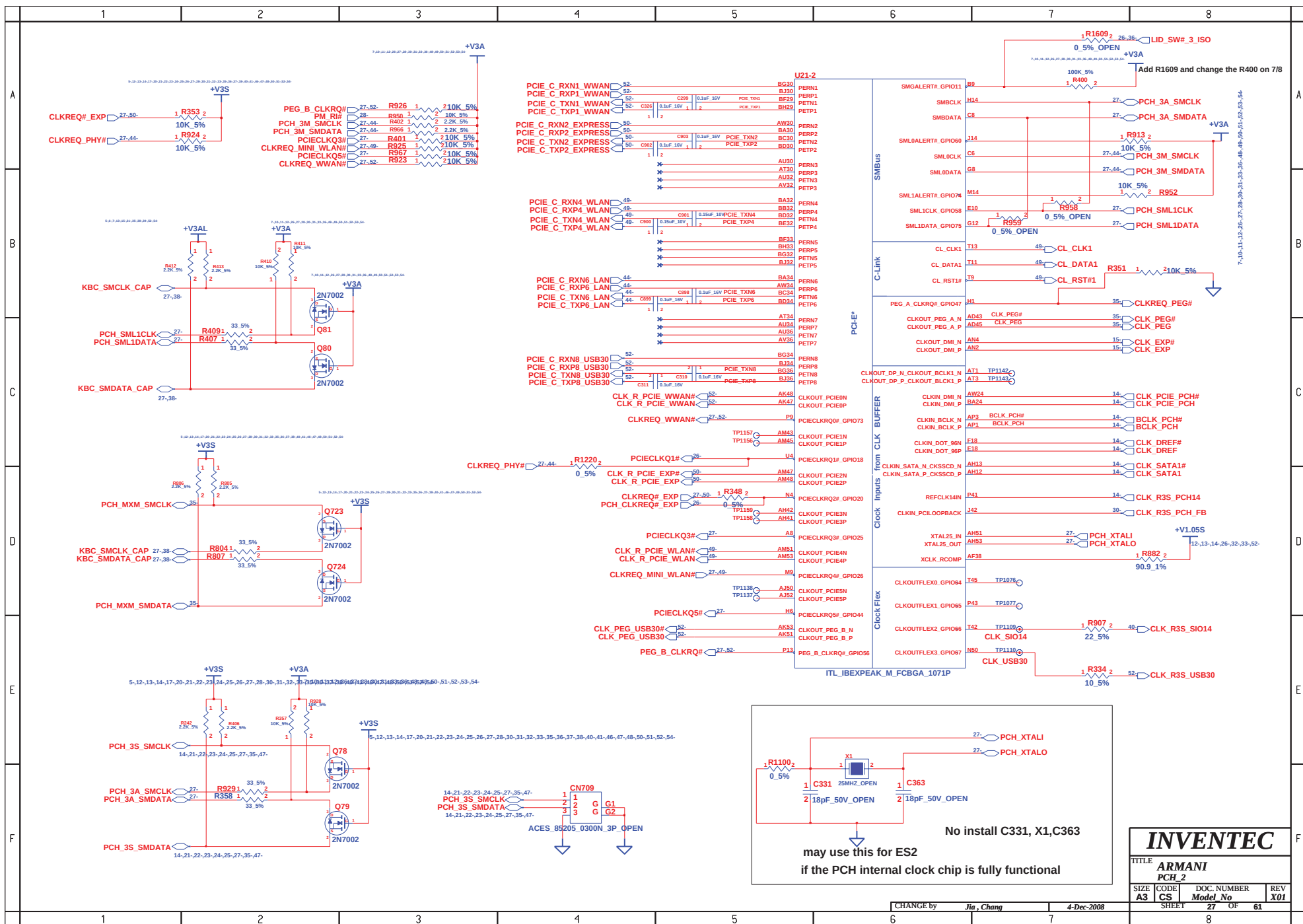


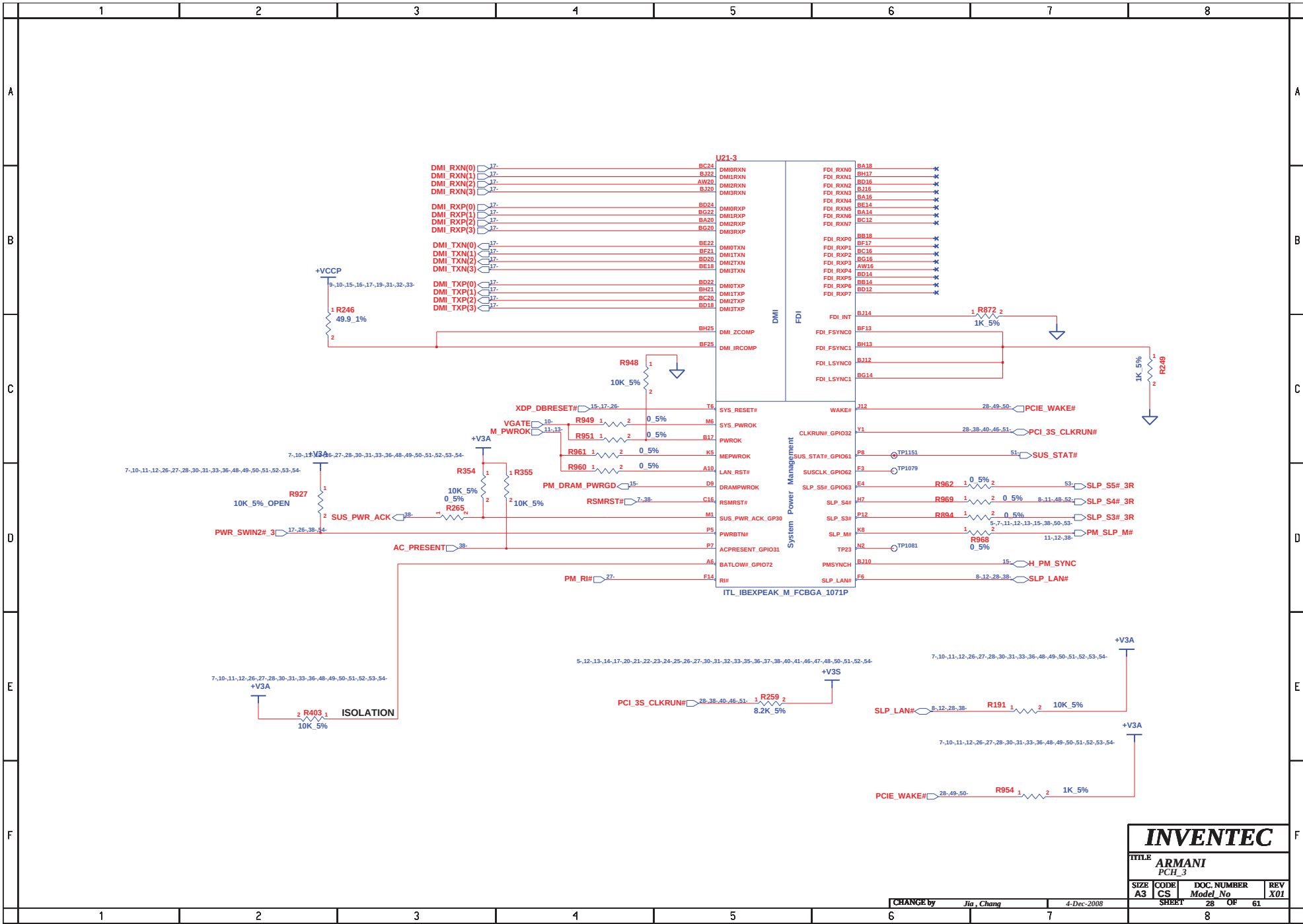


BOTTOM DIMM 1(CHANNEL B)

INVENTEC			
TITLE ARMANI			
DDR3-DIMM3			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV X01
CHANGE by Jia, Chang		24-Dec-2008	
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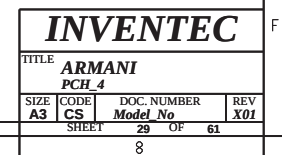
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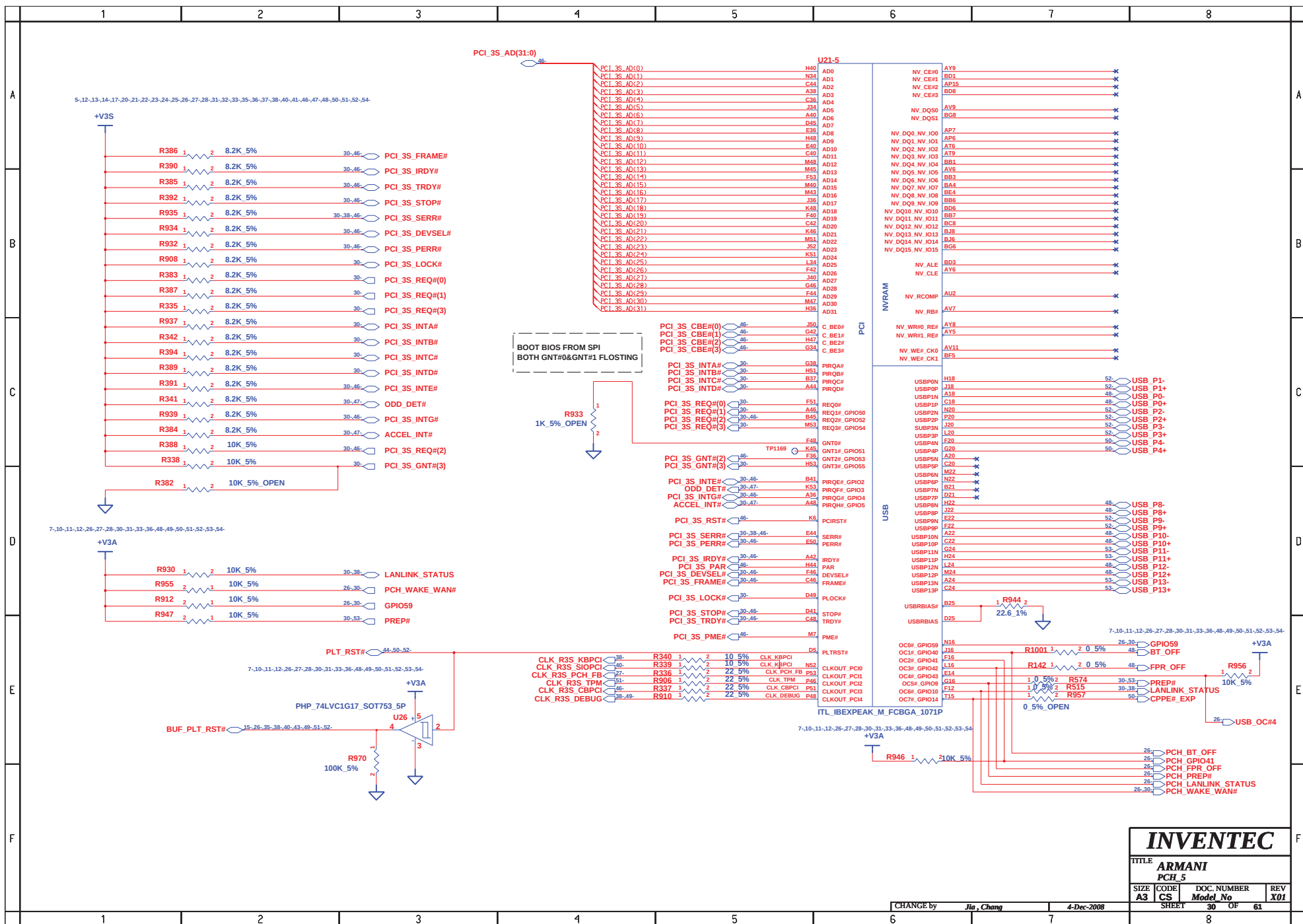
TITLE
ARMANI
PCH_3

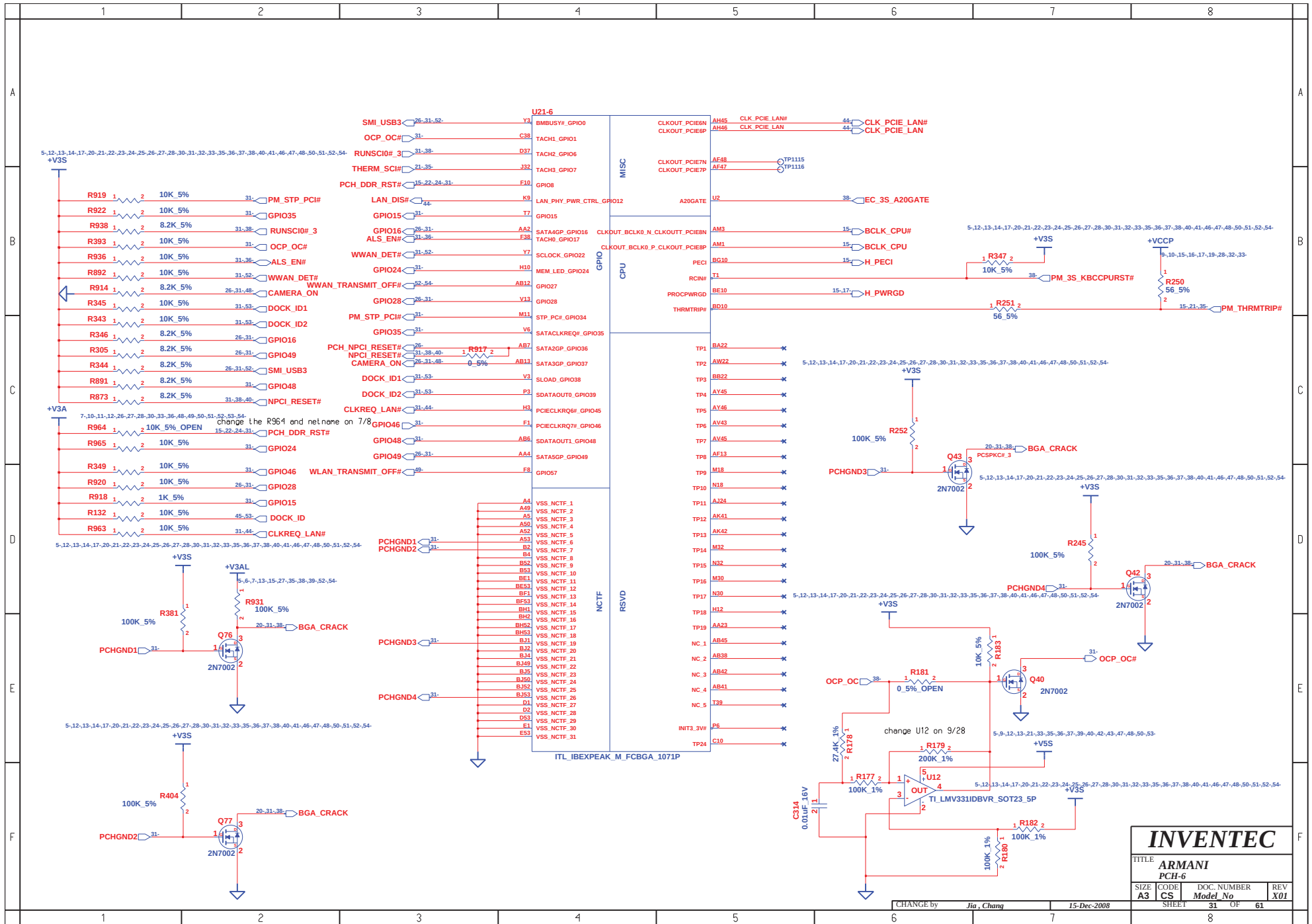
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

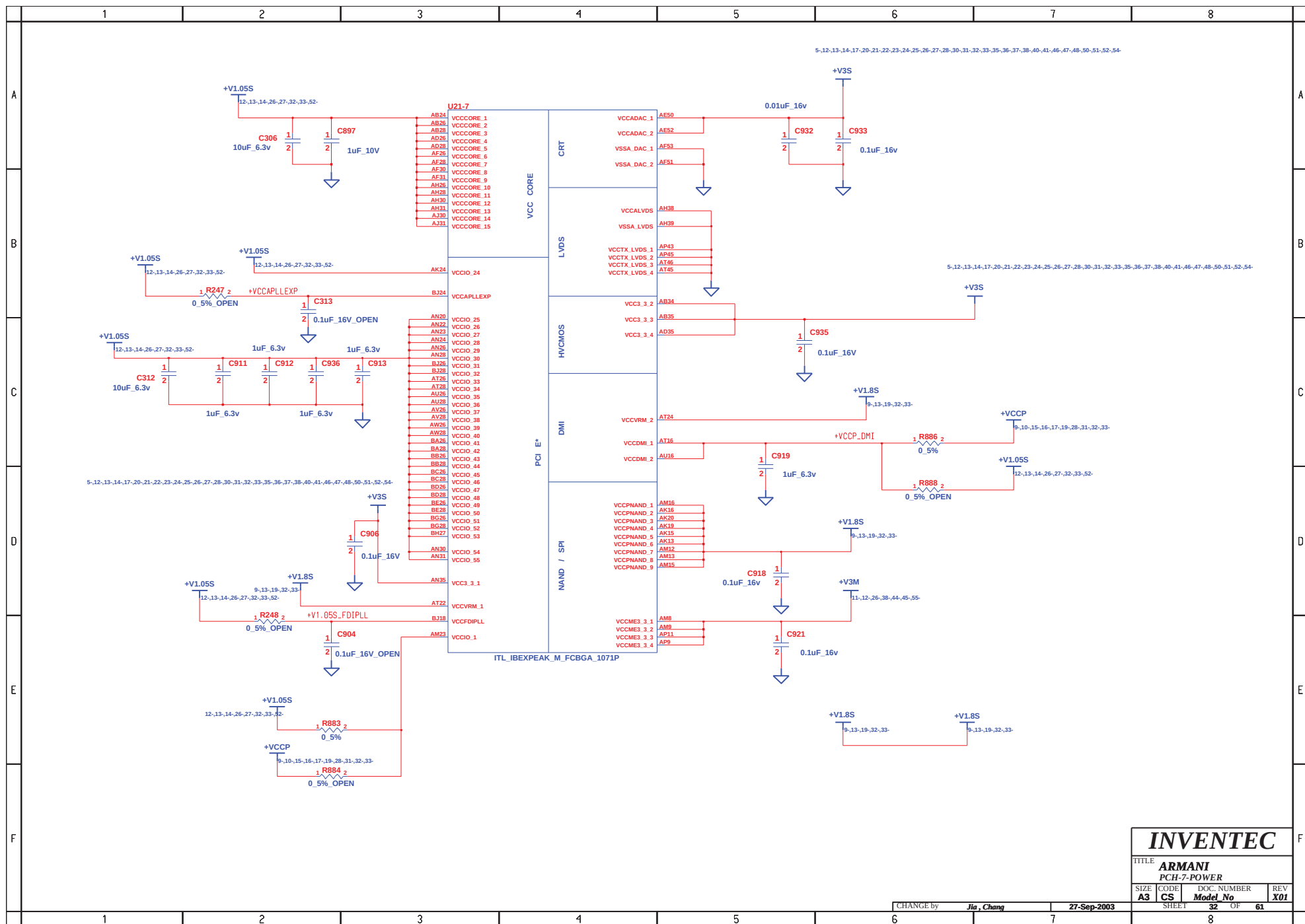
CHANGE by Jia, Chang 4-Dec-2008

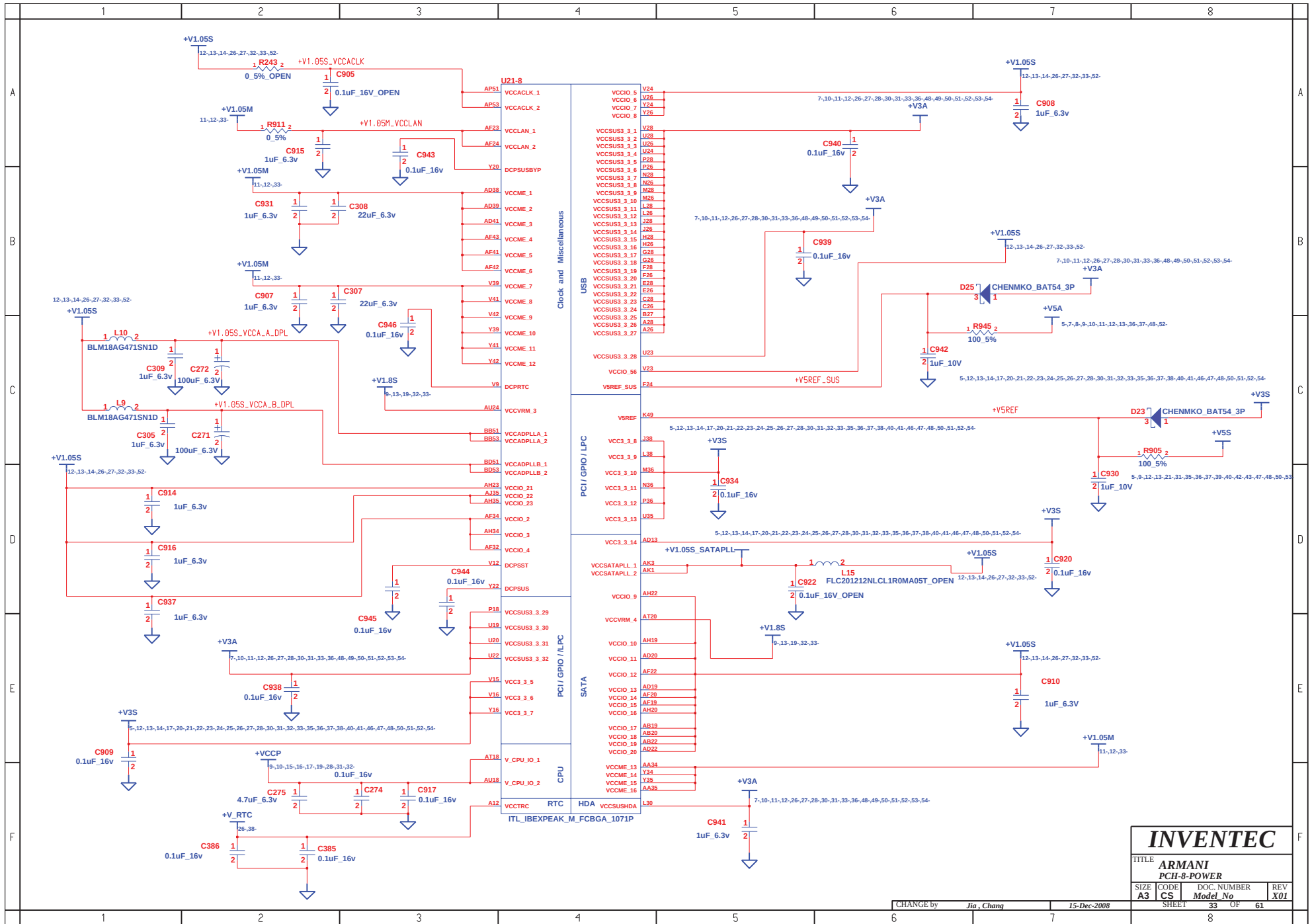
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8











INVENTEC

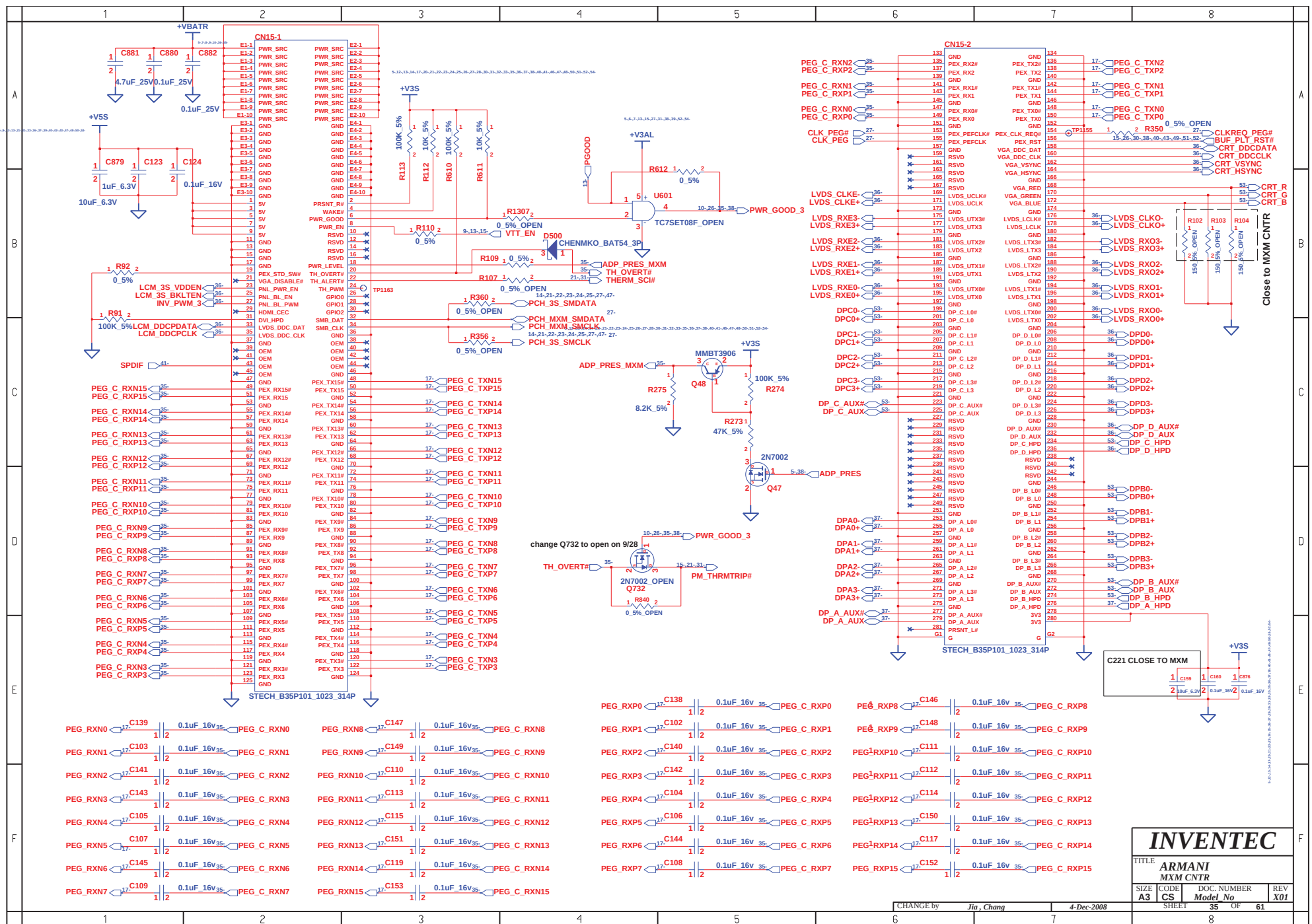
TITLE			
ARMANI			
PCH-8-POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

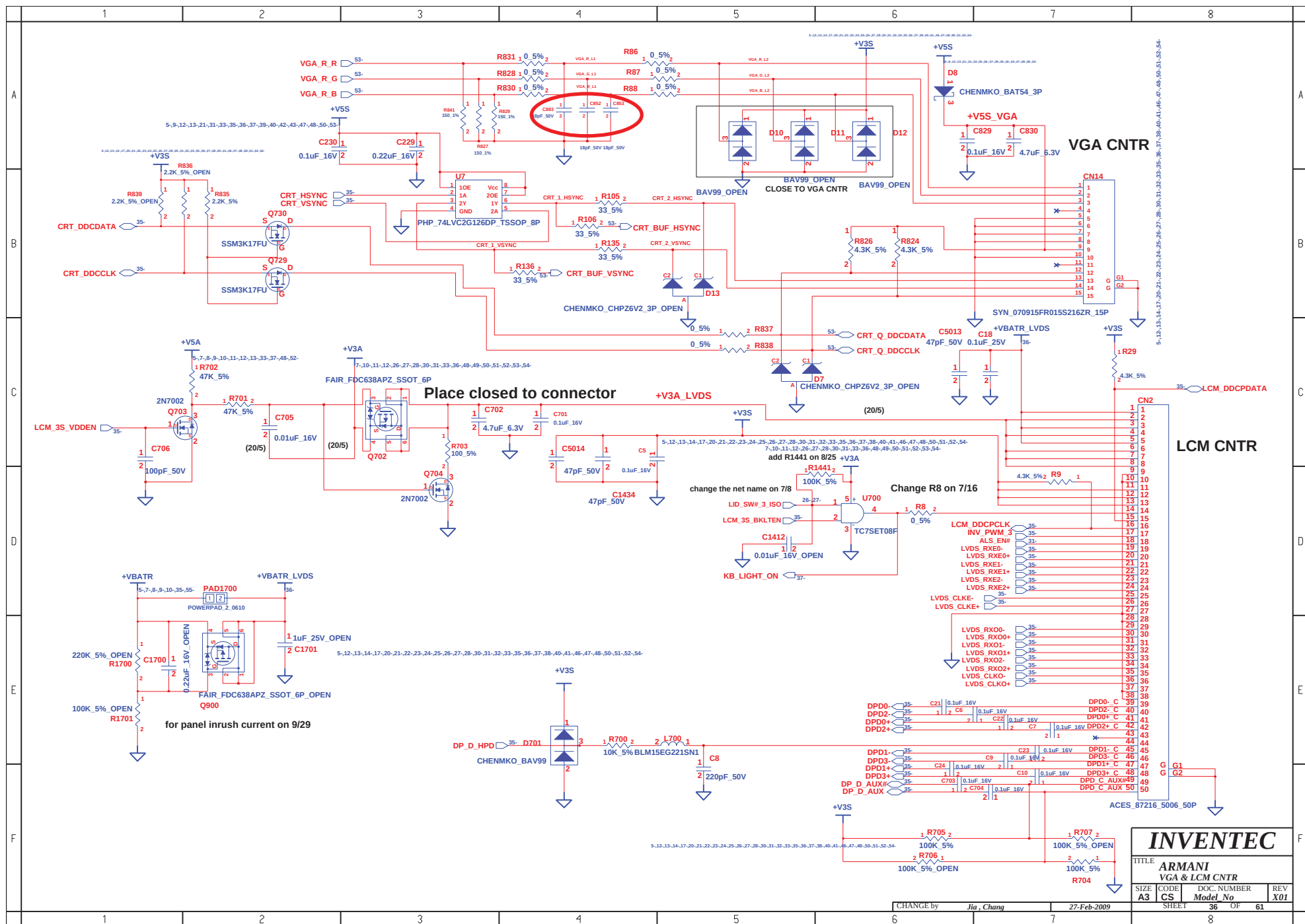
CHANGE by Jia, Chang 15-Dec-2008

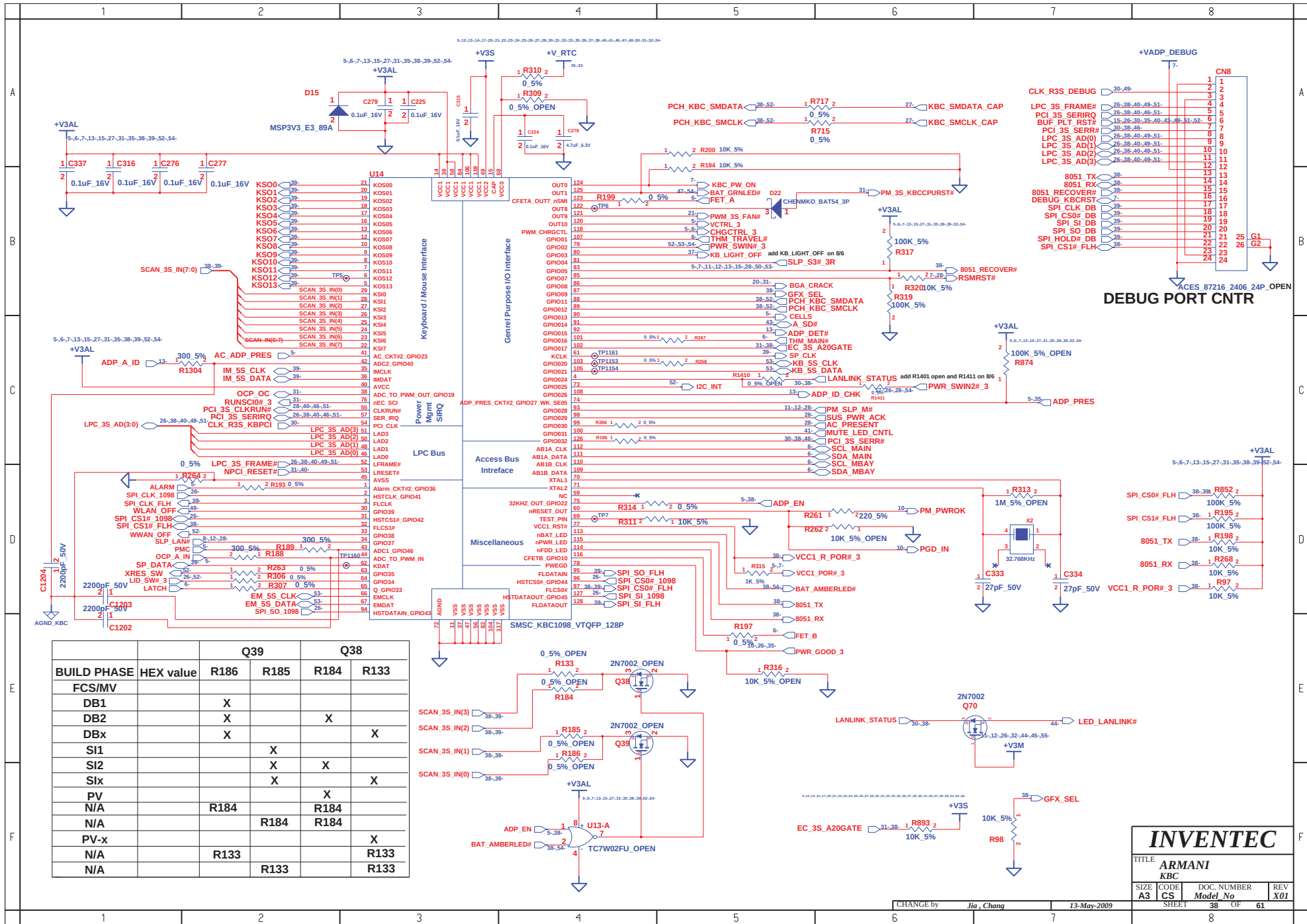
SHEET 33 OF 61

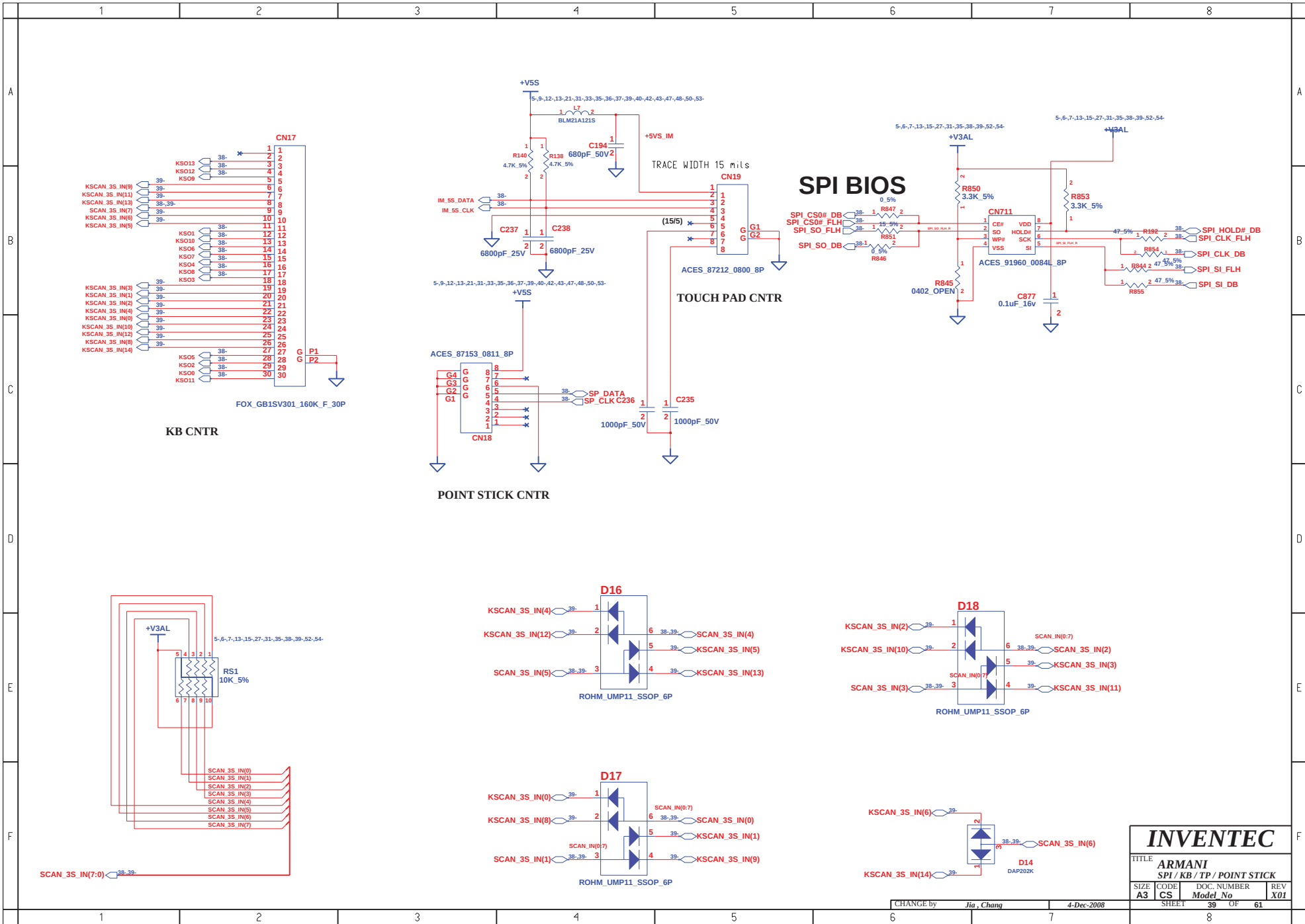
	1	2	3	4	5	6	7	8
A								
B								
C								
D								
E								
F								

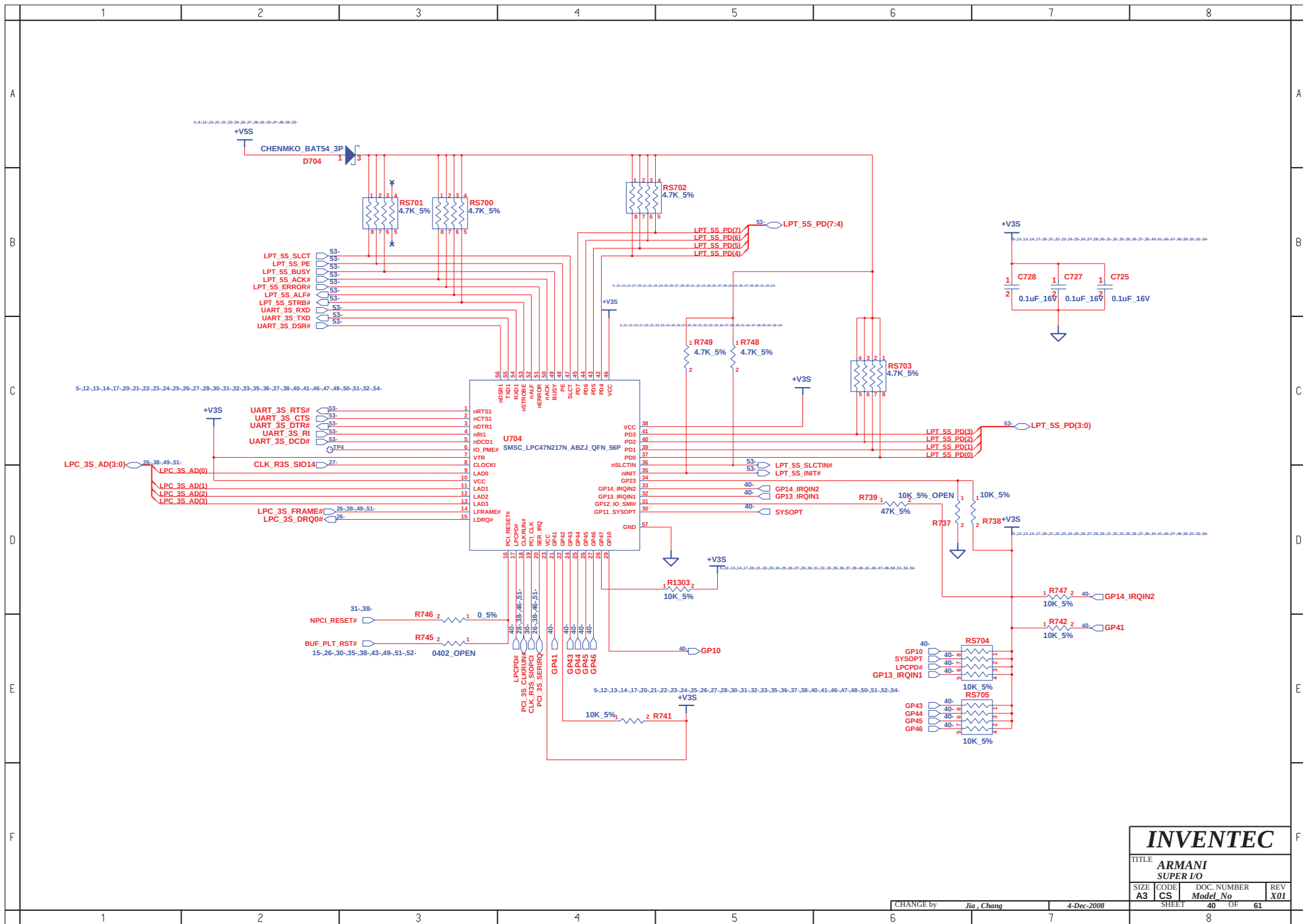
CHANGE by				Jia , Chang	15-Dec-2008	SHEET	34	OF	61
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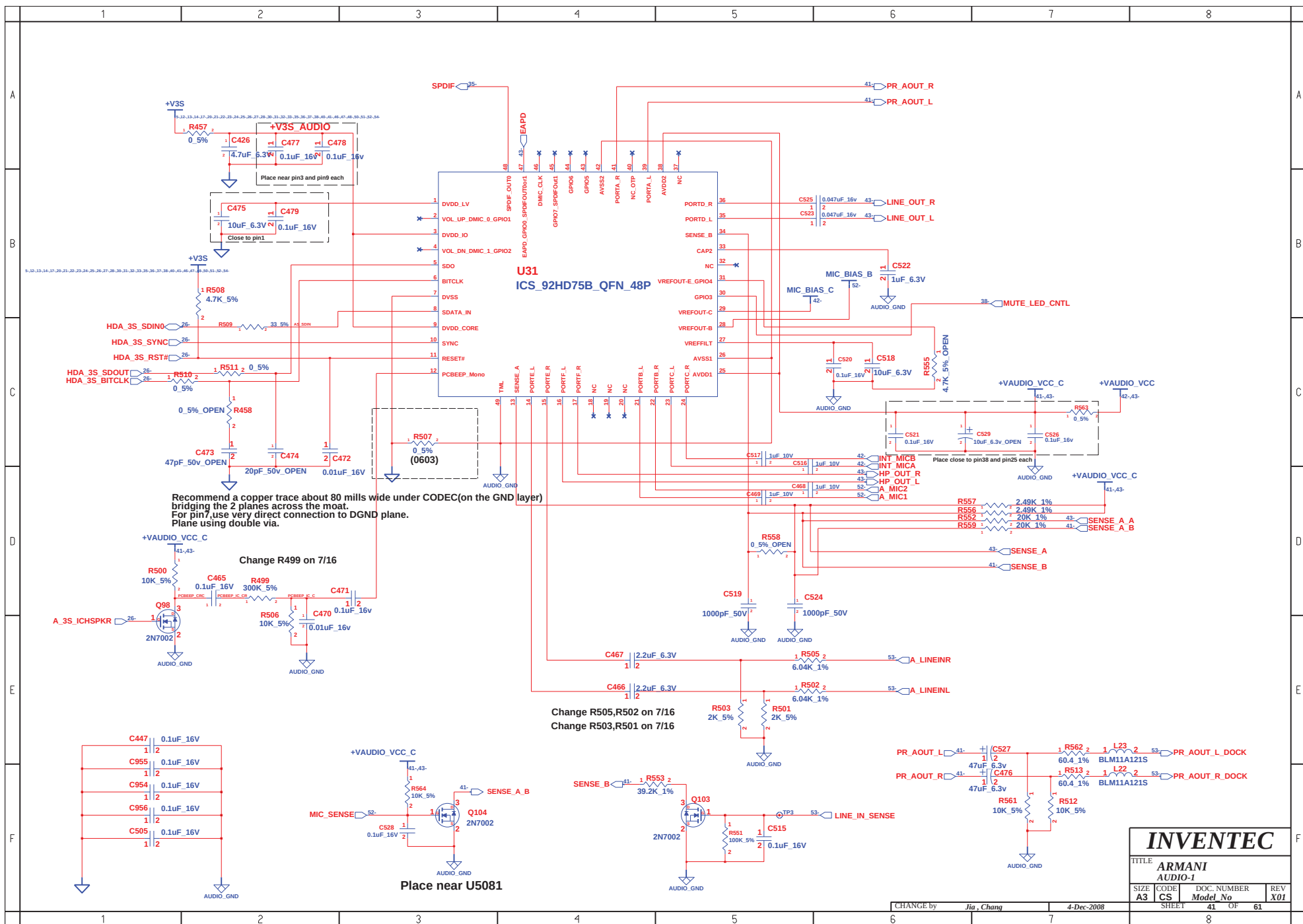
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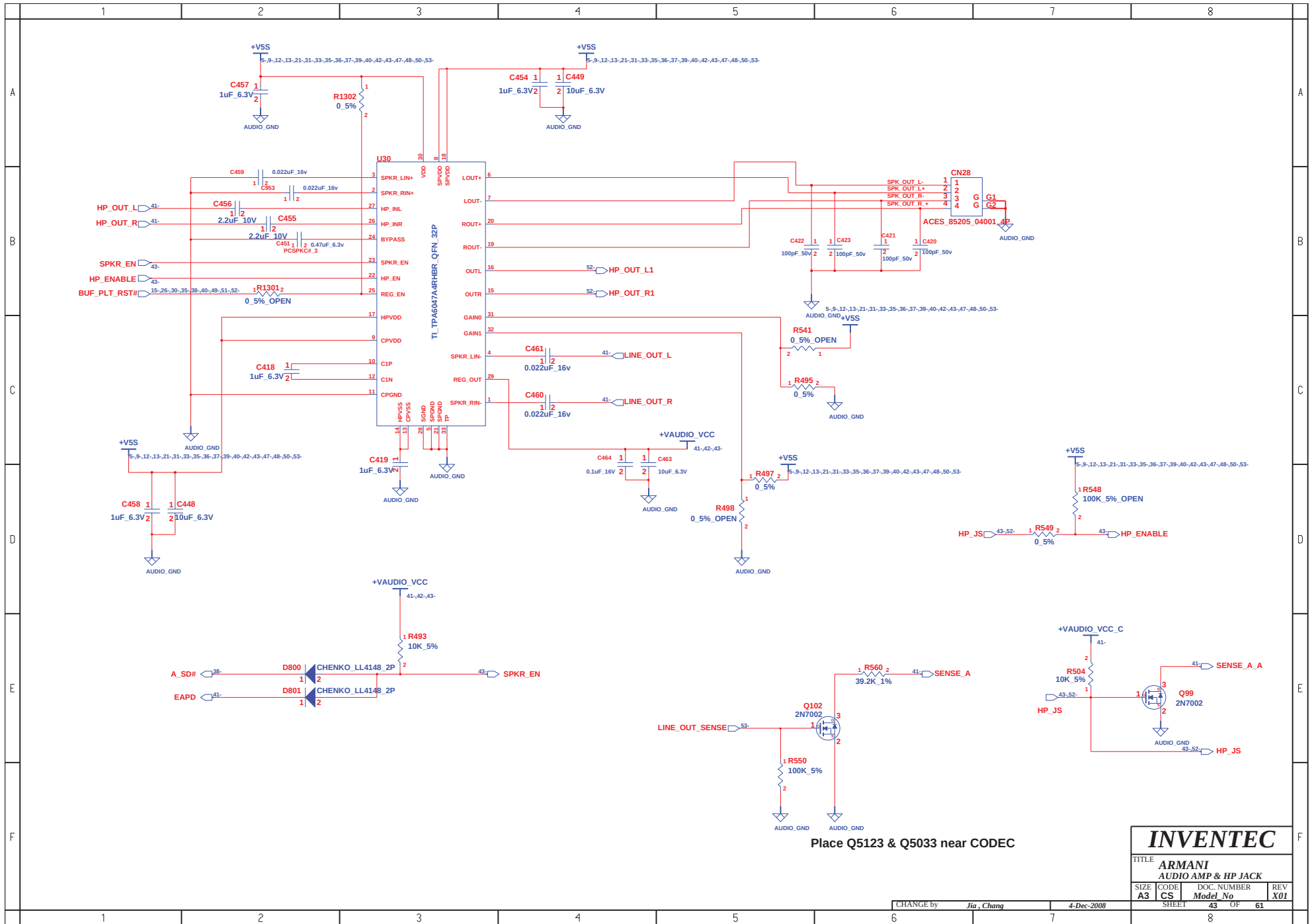
TITLE
ARMANI
SUPER I/O

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

CHANGE by Jia, Chang 4-Dec-2008

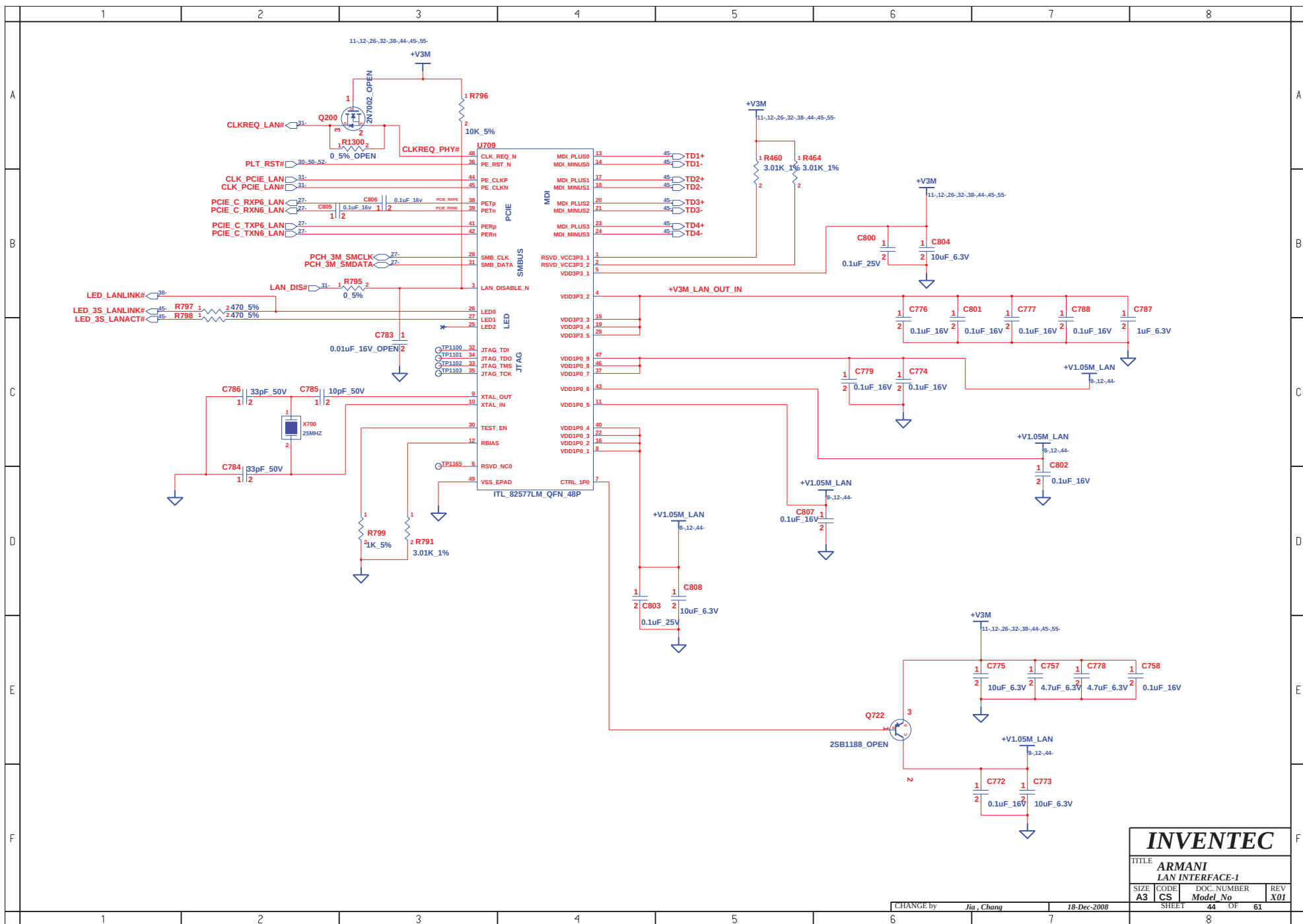
SHEET 40 OF 61

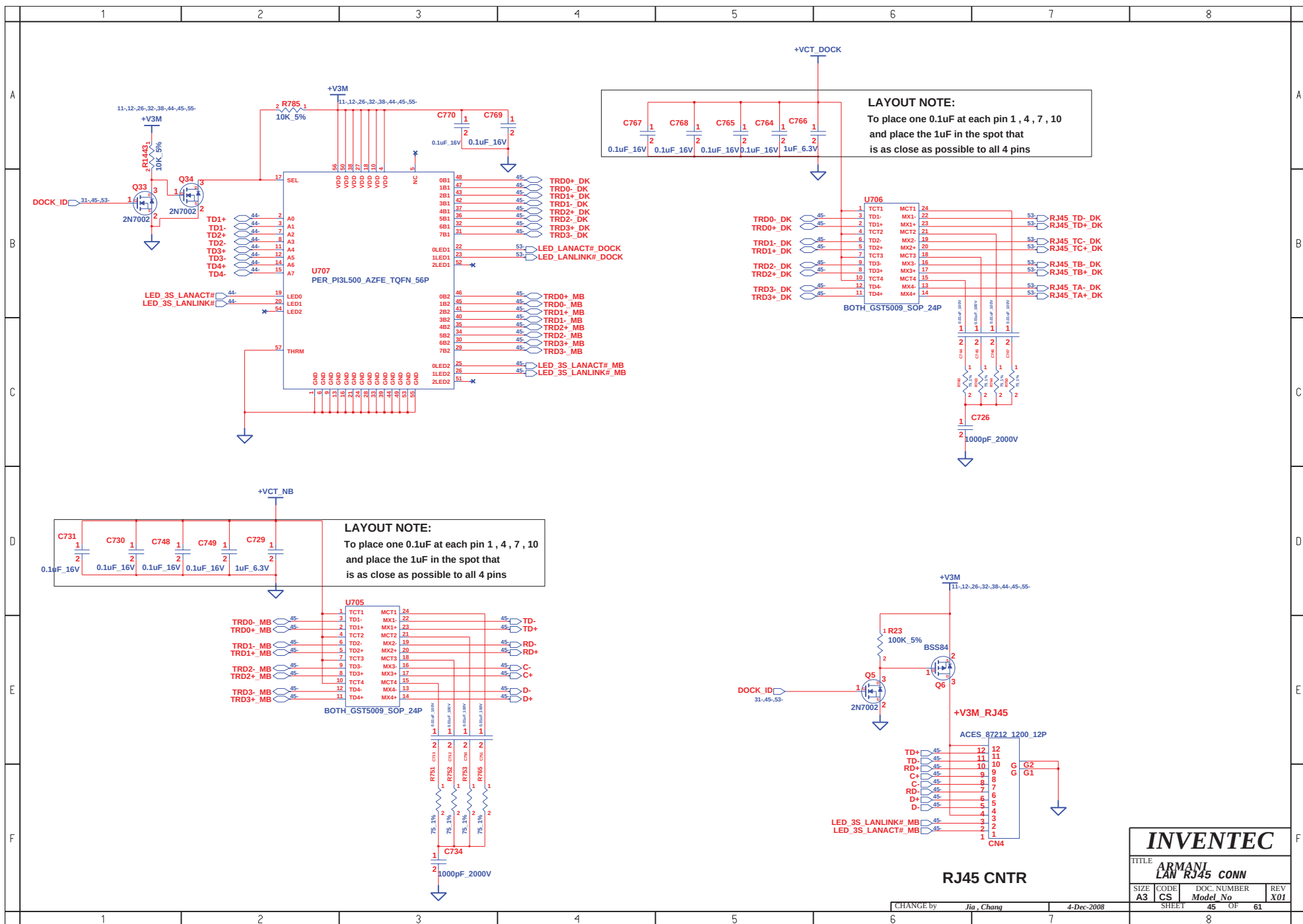


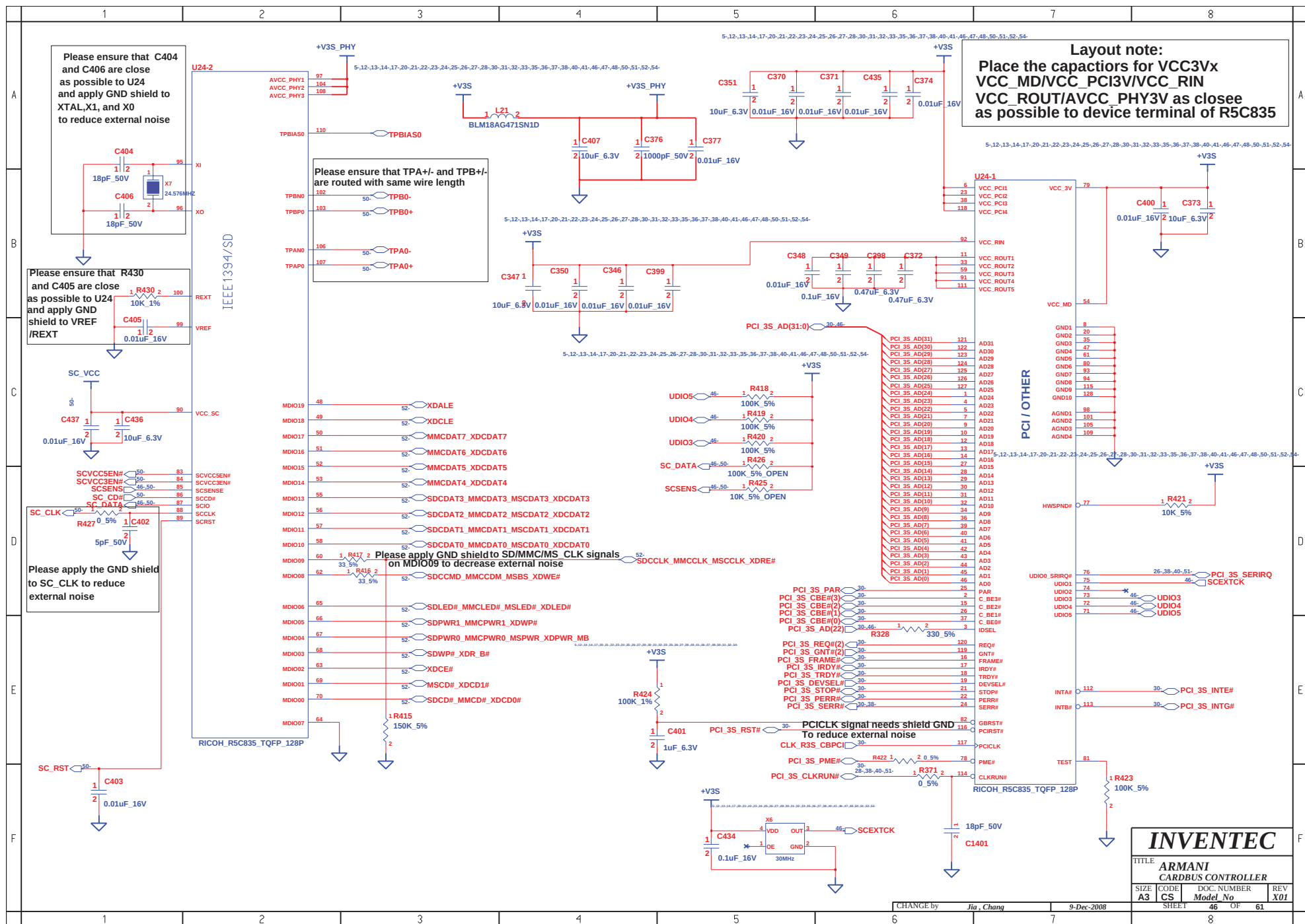


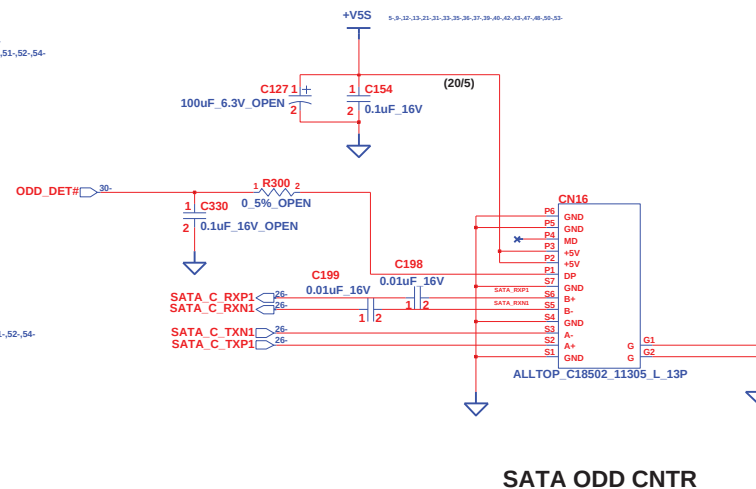
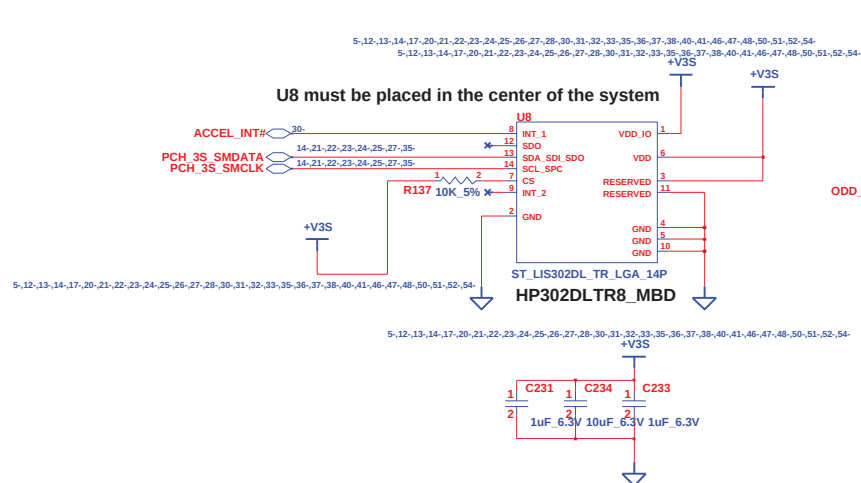
INVENTEC

TITLE			
ARMANI AUDIO AMP & HP JACK			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
CHANGE by		SHEET	OF
Jia, Chang		43	61

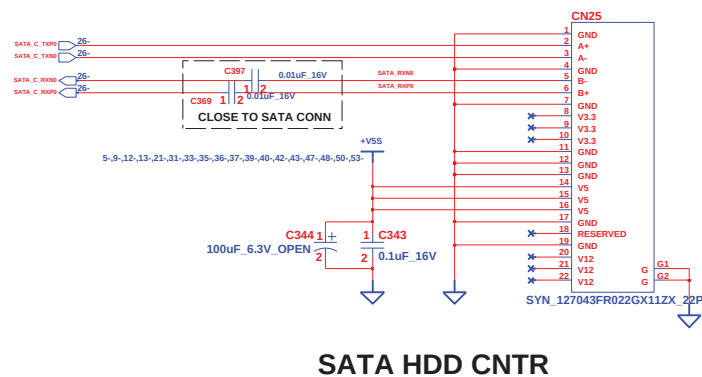
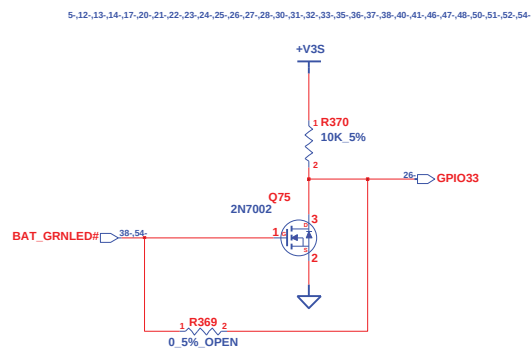








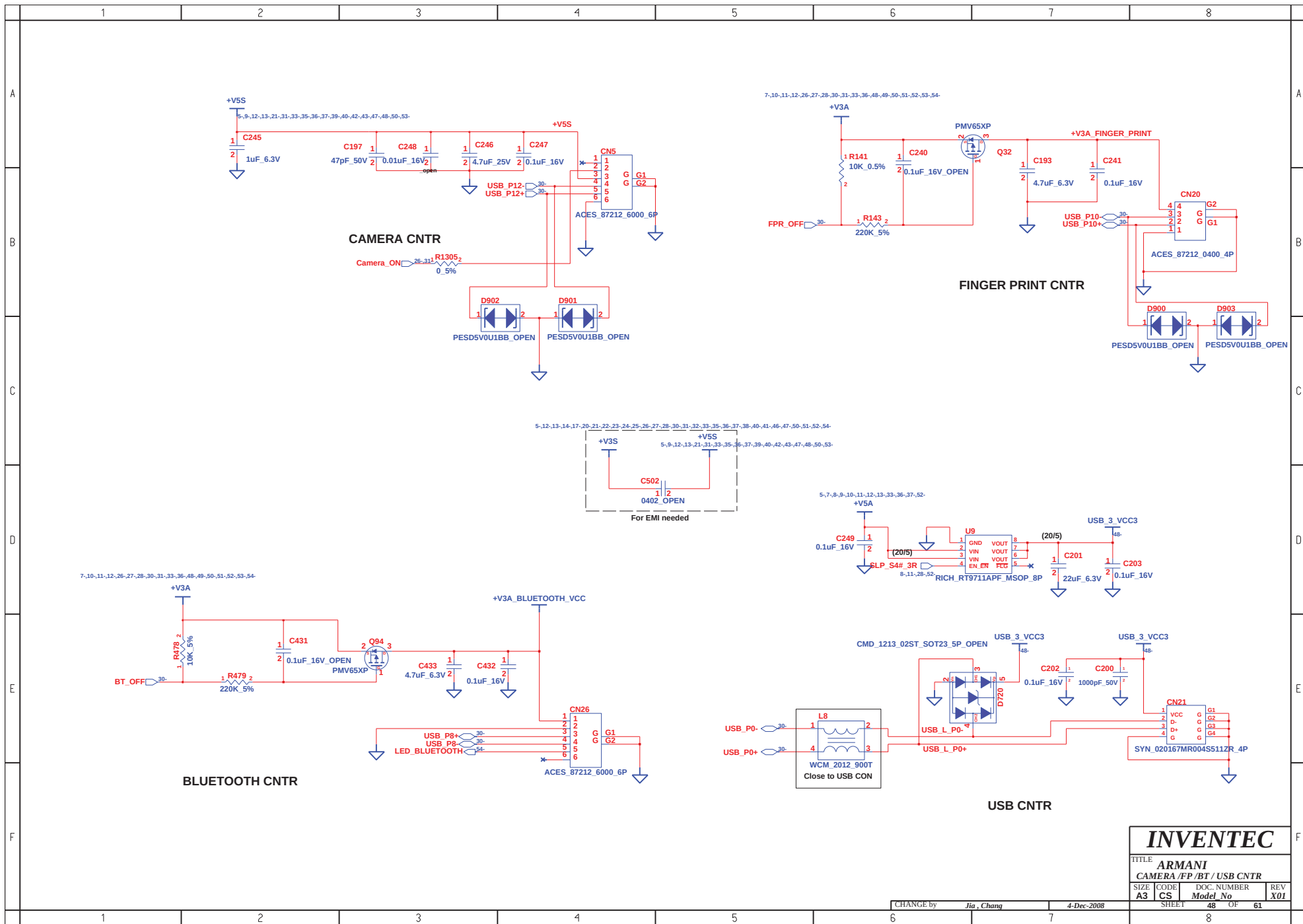
HARDDRIVE PROTECTION

**INVENTEC**

TITLE			
ARMANI HD PORTECTION/AMT ME/SATA			
SIZE	CODE	DOC. NUMBER	RE
A3	CS	<i>Model No</i>	X0

CHANGE by	<i>Jia , Chang</i>	4-Dec-2008
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CS Model No
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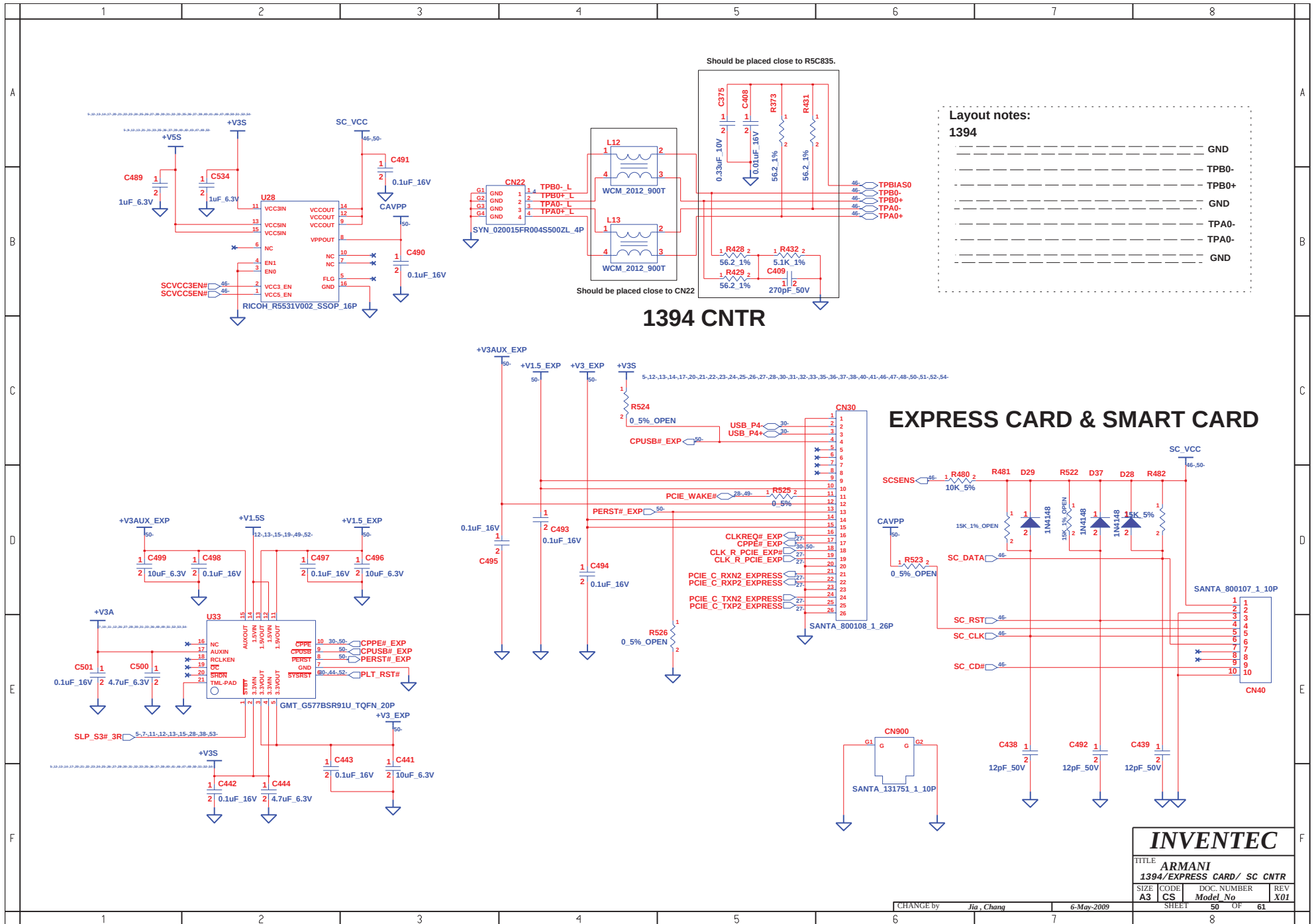


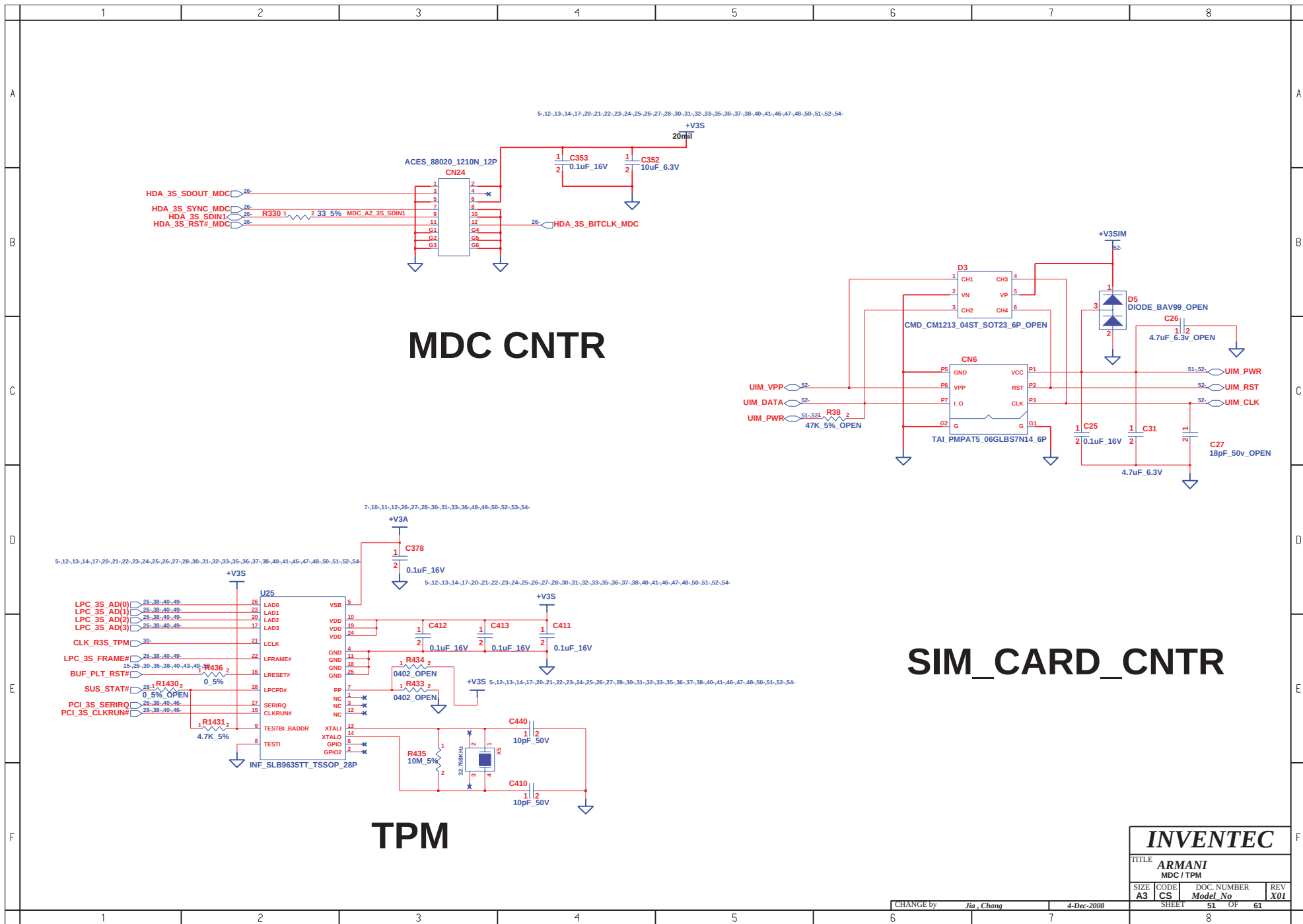


TITLE			
ARMANI WLAN CNTR			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	<i>Model No</i>	X01

CHANGE by	<i>Jia , Chang</i>	4-Dec-2008
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CS	Model No	
SHEET	49	OF 61

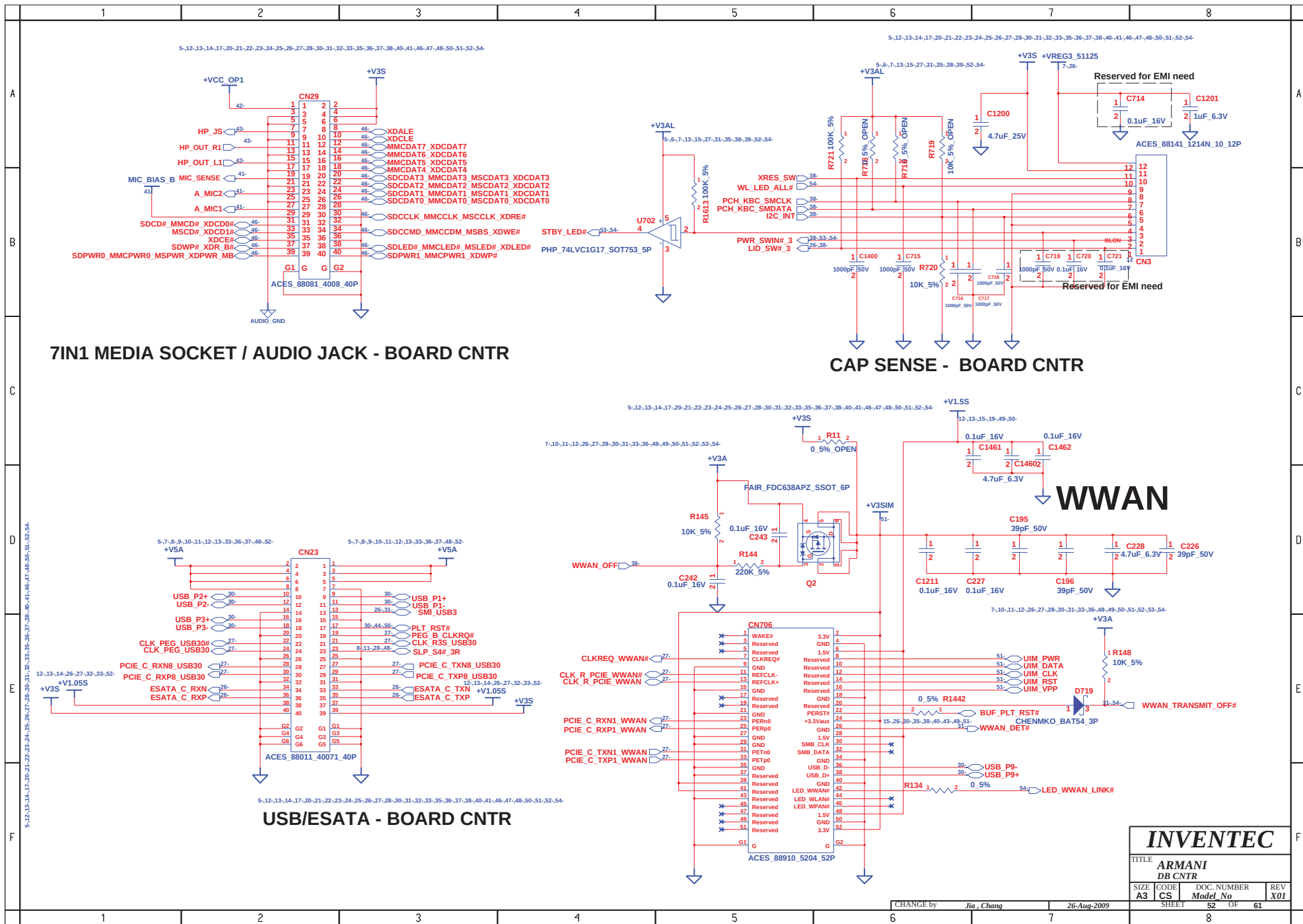


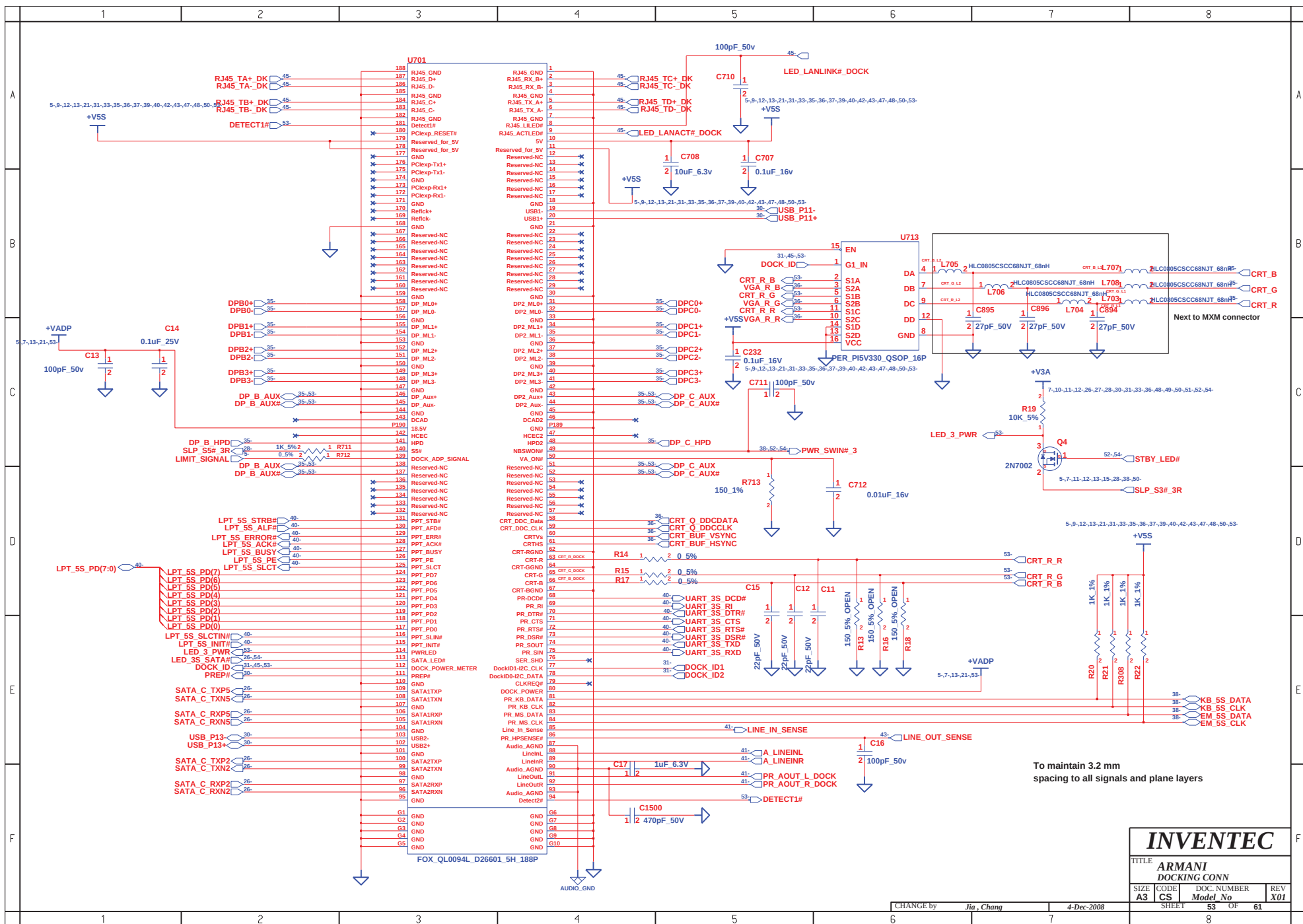


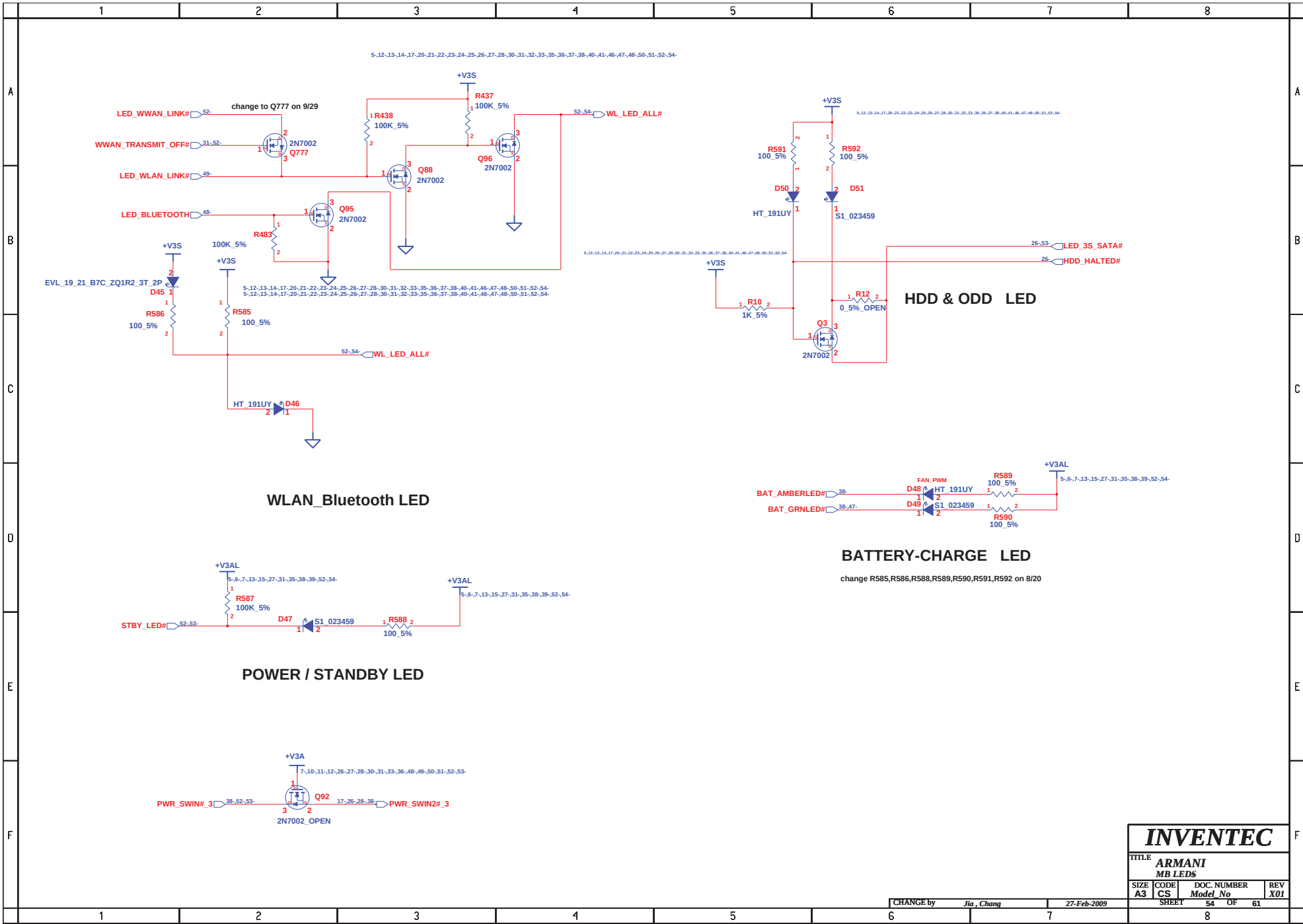
SIM_CARD_CNTR

TPM

INVENTEC			
TITLE ARMANI MDC / TPM			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV X
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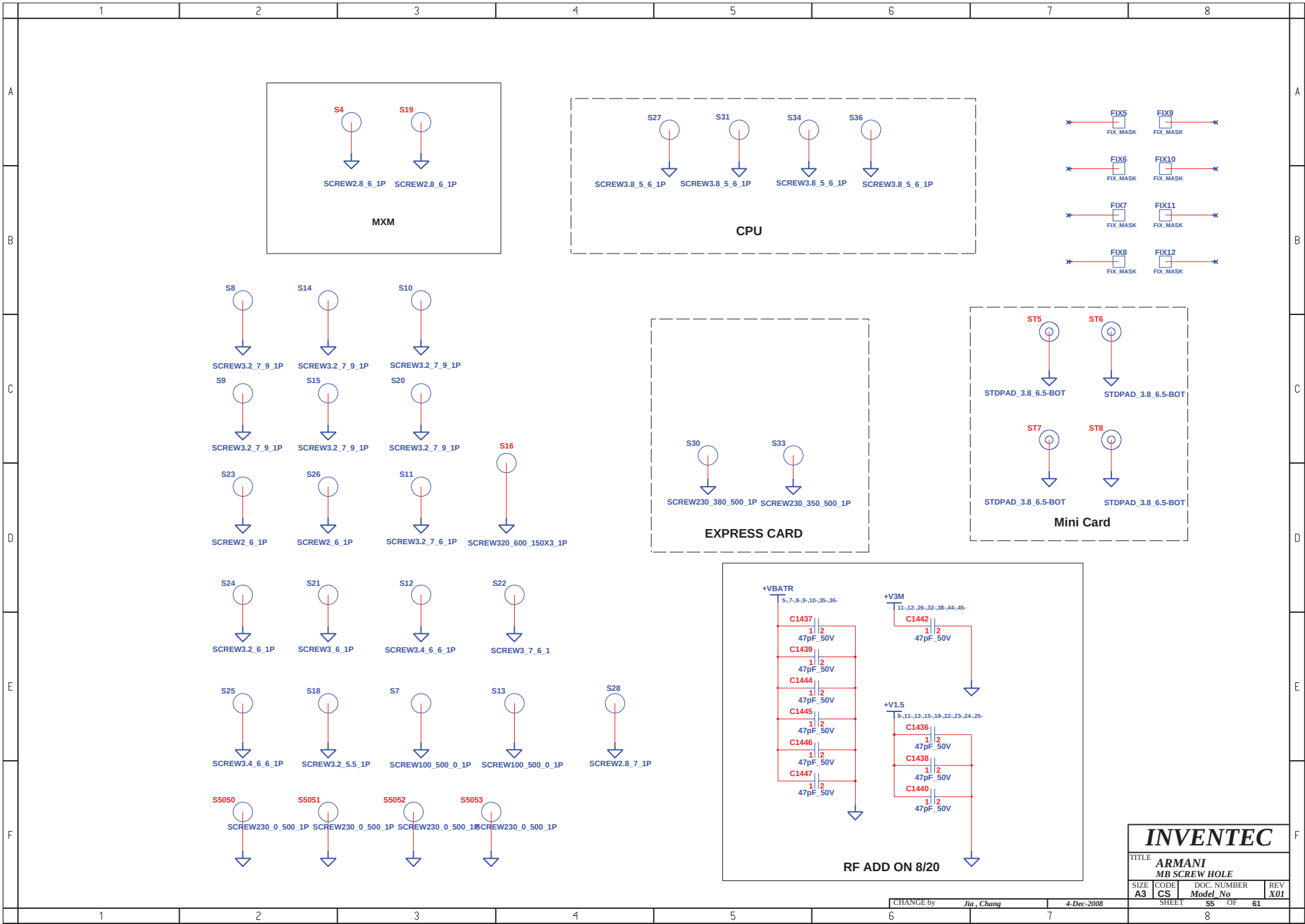




INVENTEC

TITLE			
ARMANI			
MB LEDs			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		54 OF	61

CHANGE by Jia, Chang 27-Feb-2009



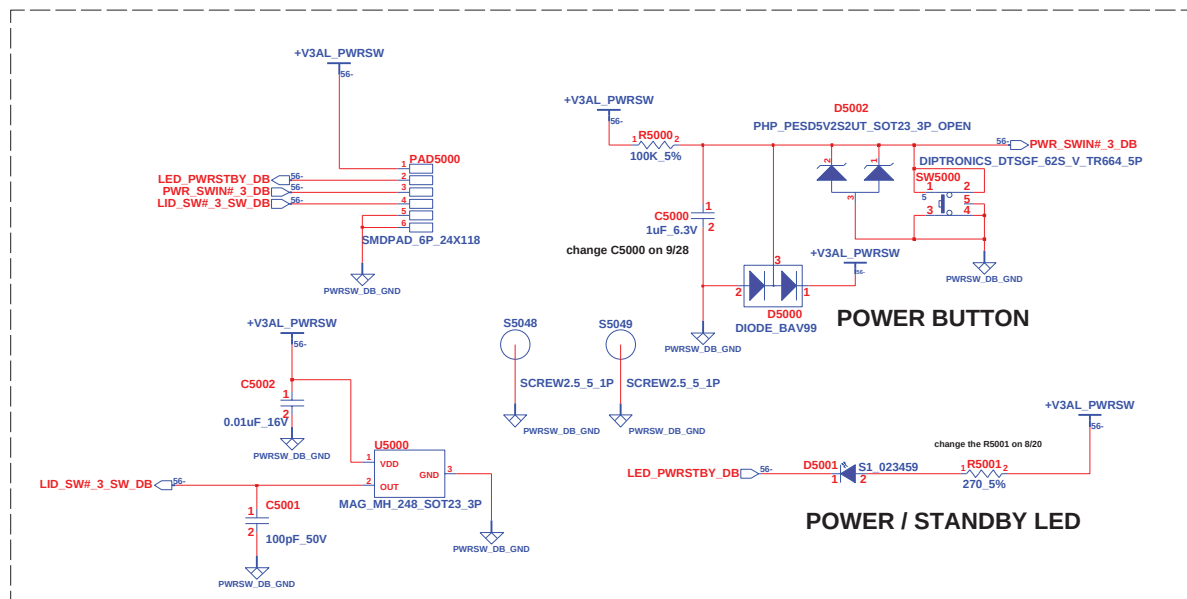
INVENTEC

TITLE
**ARMANI
MB SREW HOLE**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

CHANGE by *Jia, Chang* 4-Dec-2008

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Daughter Board - Power Button

INVENTEC

TITLE			
ARMANI			
TPM V1.2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

CHANGE by Jia, Chang 4-Dec-2008

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